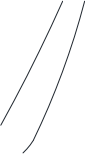
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| **JMGF073V15A**  **Description**   |  |  |  | | --- | --- | --- | | **JMG** **N-channel** **Enhancement** **Mode** **Power** **MOSFET** | | | | **Features**  150V, 43A  RDS(ON)< 7.3mΩ @ VGS =10V  Advanced Split Gate Trench Technology  Excellent RDS(ON) and Low Gate Charge  Lead free product is acquired | **Application**  Load Switch  PWM Application  Power management |  | | *100%* *UIS* *TESTED!* *100%* *ΔVds* *TESTED!* |  | | **Schematic** **Diagram**  **TO-220FP** **top** **view**  **Marking** **and** **pin** **Assignment** | | |   **Package** **Marking** **and** **Ordering** **Information**   |  |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | --- | | **Device** **Marking** | **Device** | **OUTLINE** | **Device** **Package** | | **TUBE** **(PCS)** | **Inner** **Box**  **(PCS)** | **Per** **Carton**  **(PCS)** | | JMGF073V15A | JMGF073V15A | TUBE | | TO-220FP | 50 | 1000 | 5000 |   **Absolute** **Maximum** **Ratings** (TC=25**℃** unless otherwise specified)   |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Symbol** | **Parameter** | | **Max.** | **Units** | | VDSS | Drain-Source Voltage | | 150 | V | | VGSS | Gate-Source Voltage | | ±20 | V | | ID | Continuous Drain Current | TC = 25**℃** | 43 | A | | TC = 100**℃** | 28 | A | | IDM | Pulsed Drain Current note1 | | 172 | A | | EAS | Single Pulsed Avalanche Energy note2 | | 264 | mJ | | PD | Power Dissipation | TC = 25**℃** | 30 | W | | RθJA | Thermal Resistance, Junction to Ambient | | 4.2 | **℃**/W | | TJ , TSTG | Operating and Storage Temperature Range | | -55 to +150 | **℃** |   **JieJie** **Microelectronics** **CO.** **,** **Ltd** Version :1.4  **-** **1** **-** |

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| **JMGF073V15A**  **Electrical** **Characteristics** (TJ=25**℃** unless otherwise specified)   |  |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | --- | | **Symbol** | **Parameter** | **Test** **Condition** | **Min.** | | **Typ.** | **Max.** | **Units** | | **Off** **Characteristic** | | | | | | | | | V(BR)DSS | Drain-Source Breakdown Voltage | VGS=0V, ID=250μA | 150 | | - | - | V | | IDSS | Zero Gate Voltage Drain Current | VDS=150V, VGS=0V, | - | | - | 1.0 | μA | | IGSS | Gate to Body Leakage Current | VDS=0V, VGS=±20V | - | | - | ±100 | nA | | **On** **Characteristics** | | | | | | | | | VGS(th) | Gate Threshold Voltage | VDS=VGS , ID=250μA | 2 | | 3 | 4 | V | | RDS(on) | Static Drain-Source on-Resistance note3 | VGS=10V, ID=20A | - | | 6.2 | 7.3 | mΩ | | **Dynamic** **Characteristics** | | | | | | | | | Ciss | Input Capacitance | VDS=25V, VGS=0V, f=1MHz | - | | 5596 | - | pF | | Coss | Output Capacitance | - | | 2108 | - | pF | | Crss | Reverse Transfer Capacitance | - | | 34.5 | - | pF | | Qg | Total Gate Charge | VDS=75V, ID=20A,  VGS=10V | - | | 64 | - | nC | | Qgs | Gate-Source Charge | - | | 20 | - | nC | | Qgd | Gate-Drain(“Miller”) Charge | - | | 9 | - | nC | | **Switching** **Characteristics** | | | | | | | | | td(on) | Turn-on Delay Time | VDS=50V,  ID=5A, RGEN=2Ω,  VGS=10V | - | | 10 | - | ns | | tr | Turn-on Rise Time | - | | 33 | - | ns | | td(off) | Turn-off Delay Time | - | | 27 | - | ns | | tf | Turn-off Fall Time | - | | 26 | - | ns | | **Drain-Source** **Diode** **Characteristics** **and** **Maximum** **Ratings** | | | | | | | | | IS | Maximum Continuous Drain to Source Diode Forward Current | | - | | - | 43 | A | | ISM | Maximum Pulsed Drain to Source Diode Forward Current | | - | | - | 172 | A | | VSD | Drain to Source Diode Forward Voltage | VGS=0V, IS=30A | - | | - | 1.2 | V | | trr | Body Diode Reverse Recovery Time |  | - | | 75 | - | ns | | Qrr | Body Diode Reverse Recovery Charge | IF=20A, dI/dt=100A/μs | | - | 150 | - | nC |   Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature  2. EAS condition: TJ=25**℃** , VDD=50V, VG=10V, RG=25Ω, L=0.5mH, IAS=32.5A  3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%  **JieJie** **Microelectronics** **CO.** **,** **Ltd** Version :1.4  **-** **2** **-** |



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| |  |  |  |  |  | | --- | --- | --- | --- | --- | | VDS=75V ID=20A |  |  |  |  | |  |  |  |  |  | |  |  |  |  |  | |  |  |  |  |  | |  |  | Qg(nC) |  |  |   VGS(V)  10  8  6  4  2  0   |  |  |  |  |  | | --- | --- | --- | --- | --- | |  |  |  |  |  | |  |  |  |  |  | |  | 125℃ |  | TJ=25℃ |  | |  |  |  |  |  | |  |  |  |  |  | |  |  | VSD(V) |  |  |   1.0E+01  1.0E+00  1.0E-01  1.0E-02  1.0E-03  1.0E-04  1.0E-05  8  7  6  5  4   |  |  |  |  |  | | --- | --- | --- | --- | --- | |  |  |  |  |  | |  |  |  | |  | |  |  |  |  |  | |  |  | VGS(V) |  |  |   120  90  60  30  0  120  90  60  30  0  **Figure1:** Output Characteristics  0.0 0.2 0.4 0.6 0.8 1.0  **Figure** **6:** Capacitance Characteristics  0 10 20 30 40 50  0 1.5 3.0 4.5 6.0 7.5  **Figure** **4**: Body Diode Characteristics  IS(A)  ID (A)  25℃  TJ=125℃  **JMGF073V15A**  **Typical** **Performance** **Characteristics**  **Figure** **2:** Typical Transfer Characteristics  ID (A)   |  |  |  |  |  | | --- | --- | --- | --- | --- | | 6  10V | V | 5V |  |  | |  |  |  |  |  | |  |  |  |  | VGS=4.5V | |  |  | |  |  | VDS(V) |  |  |   0 1 2 3 4 5  **Figure** **3:**On-resistance vs. Drain Current  RDS(ON) (mΩ)   |  |  |  |  | | --- | --- | --- | --- | |  |  |  |  | |  |  | VGS=10V |  | |  |  | |  |  |  |  | |  | ID(A) | |  |   0 10 20 30 40  **Figure** **5:** Gate Charge Characteristics  C(pF)  105  104 Ciss  Coss  102  101 Crss  100 VDS(V)  103  0 13 26 39 52 65  **JieJie** **Microelectronics** **CO.** **,** **Ltd** Version :1.4  **-** **3** **-** |

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| **Figure** **7:** Normalized Breakdown Voltage vs.  Junction Temperature  VBR(DSS) | **Figure** **8:** Normalized on Resistance vs.  Junction Temperature  RDS(on) | | |
| 3.25  2.5  1.75  1.0  0.25 | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  | Tj (℃) | |  |  | | |
| 0.9  1.3  1.0  1.2  1.1   |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  |  |  |  |  | |  |  | Tj (℃) | |  |  |   -100 -50 0 50 100 150 200 |
| -100 -50 0 50 100 150 200  **Figure** **10:** Maximum Continuous Drain Current vs. Case Temperature | | |
| **Figure** **9:** Maximum Safe Operating Area  ID(A**)** |
| 150  120  90  60  30  0 | | ID(A**)** |
| |  |  |  | | --- | --- | --- | | 103  102  101  100  10-1 | Limited by RDS(on)  TC=25**℃**  Single pulse  VDS (V) | 10μs  100μs  1ms  10ms  100ms  DC | |
| |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | |  |  |  |  |  |  |  | |  | |  |  |  |  |  |  |  | |  |  |  |  |  |  |  | |  |  |  |  |  |  |  | |  |  |  | Tc (℃) |  |  |  | |
| 0.1 1 10 100 0 25 50 75 100 125 150 175 | | | |
| **Figure.11:** Maximum Effective  Transient Thermal Impedance, Junction-to-Case | | | |
| ZthJ-C(**℃**/W)  PDM  10-3  10-4  100  10-1  10-2  10-6  D=0.5 D=0.2 D=0.1 D=0.05 D=0.02 D=0.01 Single pulse  TP(s)  Notes:  1.Duty factor D=t1/t2 2.Peak TJ=PDM\*ZthJC+TC  t1  t2    10-5 10-4 10-3 10-2 10-1 100 101 | | | |
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| **JMGF073V15A**  **Test** **Circuit**    **Figure1:Gate** **Charge** **Test** **Circuit** **&** **Waveform**    **Figure** **2:** **Resistive** **Switching** **Test** **Circuit** **&** **Waveforms**    **Figure** **3:Unclamped** **Inductive** **Switching** **Test** **Circuit** **&** **Waveforms**  **JieJie** **Microelectronics** **CO.** **,** **Ltd** Version :1.4  **-** **5** **-** |

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| **JMGF073V15A**  **Package** **Mechanical** **Data-TO-220FP**    Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.  Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.  Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.  This document supersedes and replaces all information previously supplied.  is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd. Copyright ©2020 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.  **JieJie** **Microelectronics** **CO.** **,** **Ltd** Version :1.4  **-** **6** **-** |