



JMSL1004BG

100V 3.4mΩ N-Ch Power MOSFET

Features

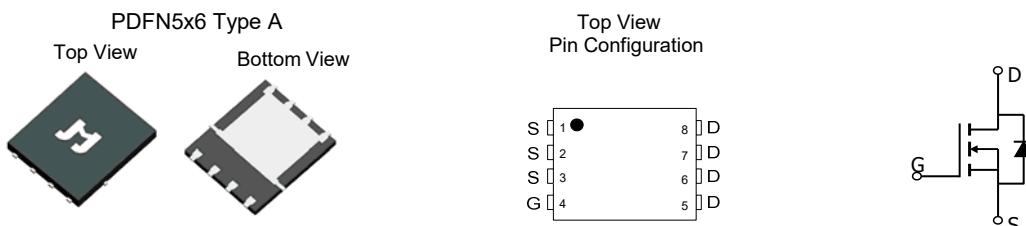
- Ultra-low $R_{DS(ON)}$
- Low Gate Charge
- High Current Capability
- 100% UIS Tested, 100% R_g Tested

Product Summary

Parameter	Typ.	Unit
V_{DS}	100	V
$V_{GS(th)}$	1.7	V
$I_D (@ V_{GS} = 10V)$ ⁽¹⁾	117	A
$R_{DS(ON)} (@ V_{GS} = 10V)$	3.4	mΩ
$R_{DS(ON)} (@ V_{GS} = 4.5V)$	4.3	mΩ

Applications

- Power Management in Telecom., Industrial Automation, CE
- Motor Driving in Power Tool, E-vehicle, Robotics
- Current Switching in DC/DC & AC/DC (SR) Sub-systems

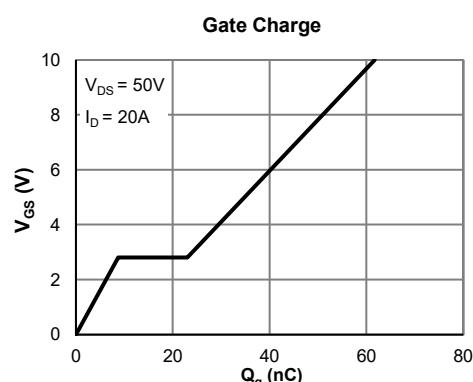
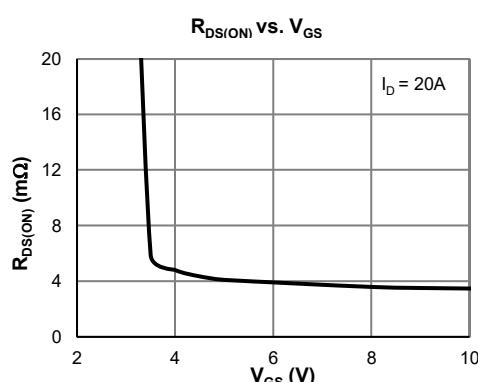


Ordering Information

Device	Package	# of Pins	Marking	MSL	T_J (°C)	Media	Quantity (pcs)
JMSL1004BG-13	PDFN5x6	8	SL1004B	1	-55 to 150	13-inch Reel	3000

Absolute Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	100	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ⁽¹⁾	I_D	117	A
		74	
Continuous Drain Current ⁽⁶⁾	I_D	100	A
		100	
Pulsed Drain Current ⁽²⁾	I_{DM}	420	A
Avalanche Current ⁽³⁾	I_{AS}	64	A
Avalanche Energy ⁽³⁾	E_{AS}	205	mJ
Power Dissipation ⁽⁴⁾	P_D	104	W
		42	
Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C



**Electrical Characteristics (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ\text{C}$			1.0	μA
					5.0	
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.2	1.7	2.5	V
Static Drain-Source ON-Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$		3.4	4.1	$\text{m}\Omega$
Static Drain-Source ON-Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 4.5\text{V}, I_D = 15\text{A}$		4.3	5.2	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{DS} = 5\text{V}, I_D = 20\text{A}$		86		S
Diode Forward Voltage	V_{SD}	$I_S = 1\text{A}, V_{GS} = 0\text{V}$		0.7	1.0	V
Diode Continuous Current	I_S	$T_C = 25^\circ\text{C}$			104	A
DYNAMIC PARAMETERS (5)						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, V_{DS} = 50\text{V}, f = 1\text{MHz}$		3709		pF
Output Capacitance	C_{oss}			873		pF
Reverse Transfer Capacitance	C_{rss}			6.7		pF
Gate Resistance	R_g	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$		2.4		Ω
SWITCHING PARAMETERS (5)						
Total Gate Charge (@ $V_{GS} = 10\text{V}$)	Q_g	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 50\text{V}, I_D = 20\text{A}$		62		nC
Total Gate Charge (@ $V_{GS} = 6.0\text{V}$)	Q_g			40		nC
Gate Source Charge	Q_{gs}			8.8		nC
Gate Drain Charge	Q_{gd}			14.3		nC
Turn-On DelayTime	$t_{D(\text{on})}$	$V_{GS} = 10\text{V}, V_{DS} = 50\text{V}$ $R_L = 2.5\Omega, R_{\text{GEN}} = 6\Omega$		11.6		ns
Turn-On Rise Time	t_r			18.9		ns
Turn-Off DelayTime	$t_{D(\text{off})}$			68		ns
Turn-Off Fall Time	t_f			46		ns
Body Diode Reverse Recovery Time	t_{rr}		$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	75		ns
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		166		nC

Thermal Performance

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	48	58	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.90	1.2	$^\circ\text{C/W}$

Notes:

1. Computed continuous current assumes the condition of $T_{J_{\text{Max}}}$ while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under $T_{J_{\text{Max}}} = 150^\circ\text{C}$.
3. This single-pulse measurement was taken under the following condition [$L = 100\text{mH}, V_{GS} = 10\text{V}, V_{DS} = 50\text{V}$] while its value is limited by $T_{J_{\text{Max}}} = 150^\circ\text{C}$.
4. The power dissipation P_D is based on $T_{J_{\text{Max}}} = 150^\circ\text{C}$.
5. This value is guaranteed by design hence it is not included in the production test.
6. Continuous current rating is limited by the package used.

Typical Electrical & Thermal Characteristics

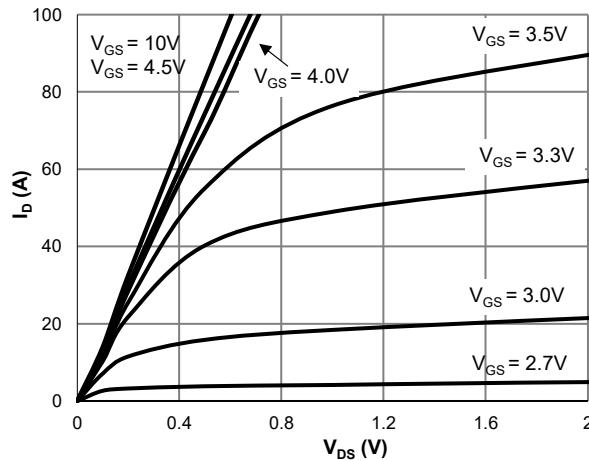


Figure 1: Saturation Characteristics

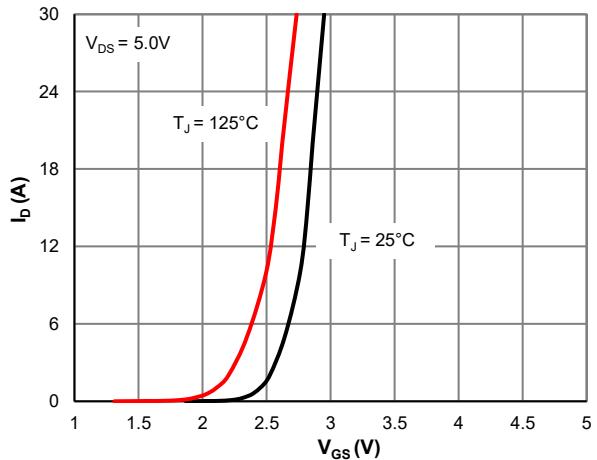


Figure 2: Transfer Characteristics

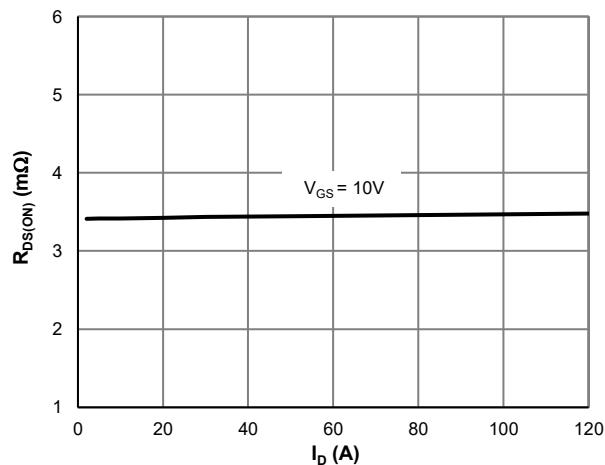


Figure 3: $R_{DS(ON)}$ vs. Drain Current

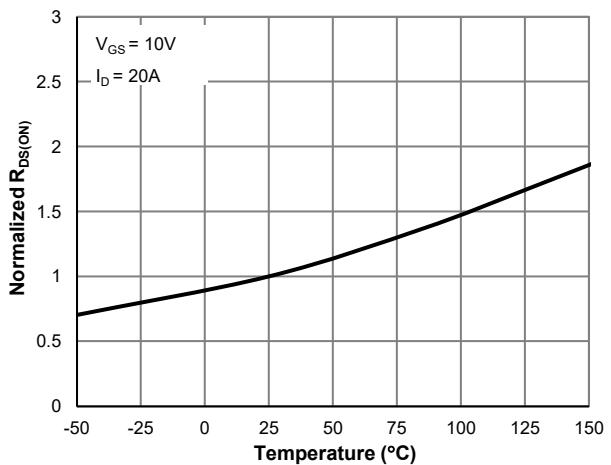


Figure 4: $R_{DS(ON)}$ vs. Junction Temperature

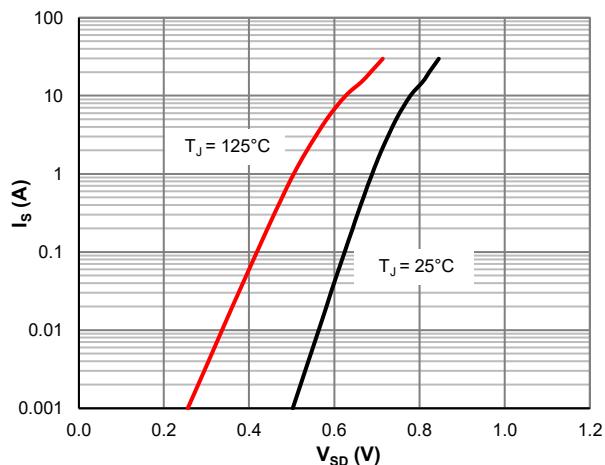


Figure 5: Body-Diode Characteristics

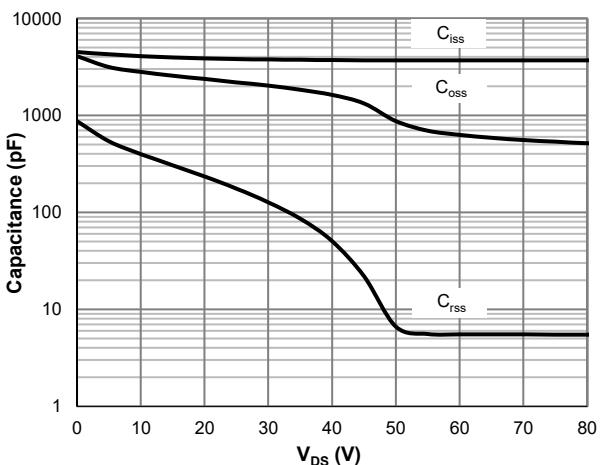


Figure 6: Capacitance Characteristics

Typical Electrical & Thermal Characteristics

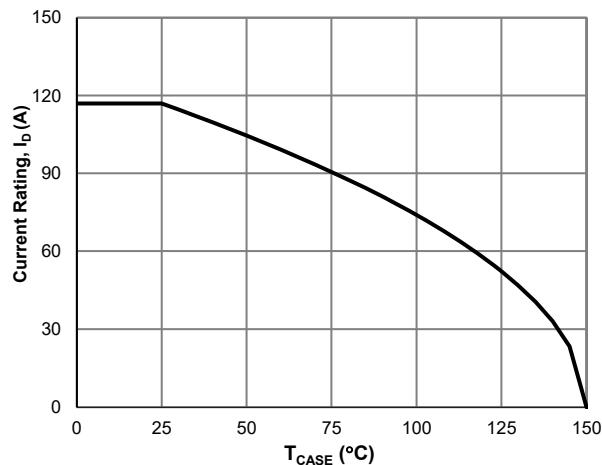


Figure 7: Current De-rating

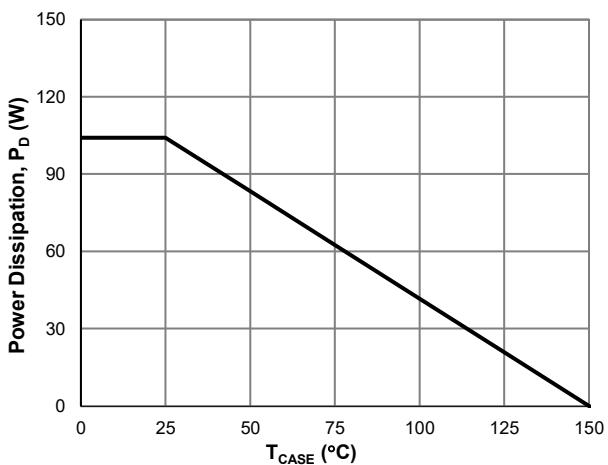


Figure 8: Power De-rating

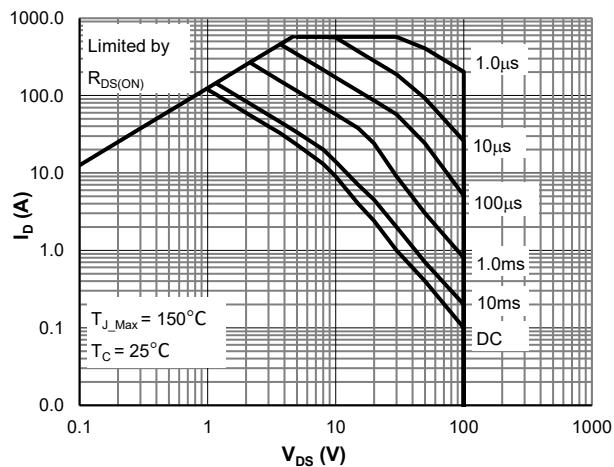


Figure 9: Maximum Safe Operating

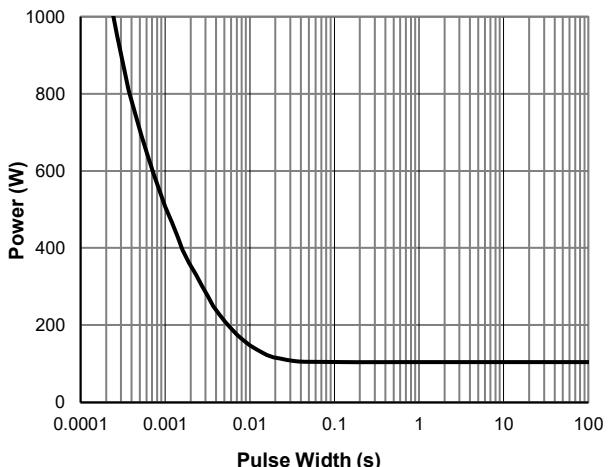


Figure 10: Single Pulse Power Rating, Junction-to-Case

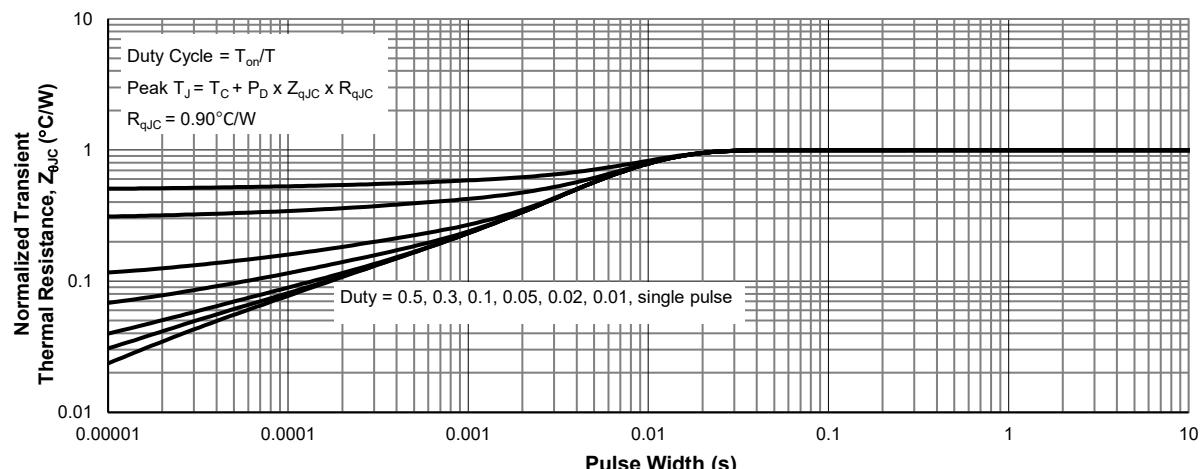
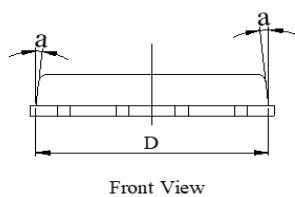
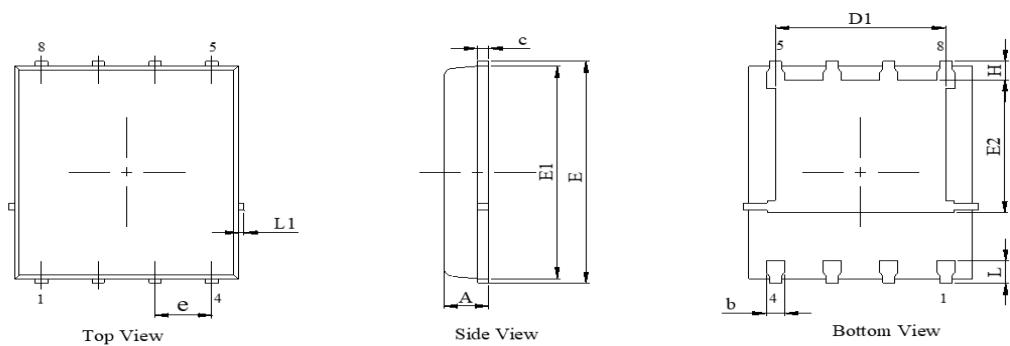


Figure 11: Normalized Maximum Transient Thermal Impedance

PDFN5x6 Package Information

Package Outline



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. ALL DIMNESIONS IN MILLIMETER (ANGLE IN DEGREE).
3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

DIM	MILLIMETER	
	MIN.	MAX.
A	0.90	1.20
b	0.33	0.51
c	0.23	0.33
D	4.80	5.40
D1	3.61	4.25
E	5.90	6.30
E1	5.55	5.95
E2	3.35	3.95
e	1.27BSC	
H	0.41	0.80
L	0.51	0.80
L1	-	0.15
a	0°	12°

Recommended Footprint

