

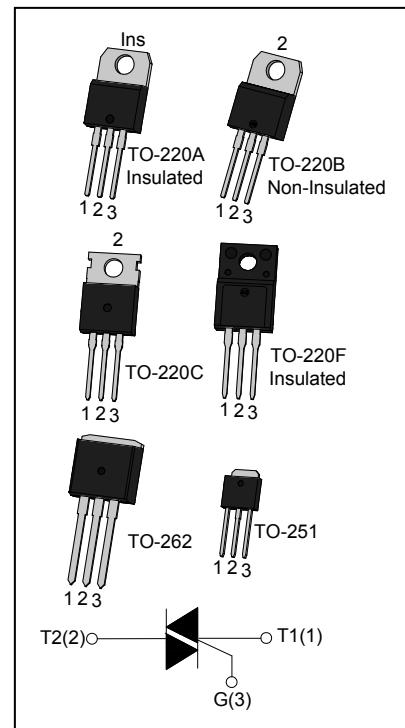


JST137 Series 8A TRIACS

Rev.13.0

DESCRIPTION:

JST137 series triacs with low holding and latching current are especially recommended for use on middle and small resistance type power load. From all three terminals to external heatsink, JST137A provides a rated insulation voltage of 2500 V_{RMS}, and JST137F provides a rated insulation voltage of 2000 V_{RMS}, complying with UL standards (File ref: E252906). All the packages listed above are RoHS compliant. (2011/65/EU)



MAIN FEATURES

Symbol	Value	Unit
I _{T(RMS)}	8	A
V _{DRM/V_{RRM}}	600 and 800	V

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T _{stg}	-40-150	°C
Operating junction temperature range	T _j	-40-125	°C
Repetitive peak off-state voltage(T _j =25°C)	V _{DRM}	600/800	V
Repetitive peak reverse voltage(T _j =25°C)	V _{RRM}	600/800	V
RMS on-state current	I _{T(RMS)}	8	A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I _{TSM}	65	A
I ² t value for fusing (tp=10ms)	I ² t	21	A ² s
Peak gate current	I _{GM}	2	A

Critical rate of rise of on-state current($I_G=2 \times I_{GT}$)	I - II - III	dI/dt	50	A/ μ s
	IV		10	
Average gate power dissipation		$P_{G(AV)}$	0.5	W
Peak gate power		P_{GM}	5	W

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant		Value				Unit
				D	E	F	G	
I_{GT}	$V_D=12\text{V}$ $R_L=30\Omega$	I - II - III	MAX	5	10	25	50	mA
		IV		10	25	70	100	
V_{GT}	ALL		MAX	1.3				V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^\circ\text{C}$ $R_L=3.3\text{k}\Omega$	ALL	MIN	0.2				V
I_L	$I_G=1.2I_{GT}$	I - III	MAX	10	20	50	70	mA
		II - IV		20	30	70	100	
I_H	$I_T=100\text{mA}$		MAX	10	15	40	60	mA
dV/dt	$V_D=2/3V_{DRM}$ Gate Open $T_j=125^\circ\text{C}$		MIN	20	50	50	200	V/ μ s

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V_{TM}	$I_{TM}=10\text{A}$	$t_p=380\mu\text{s}$	1.6	V
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$		5	μA
I_{RRM}	$T_j=25^\circ\text{C}$		1	mA

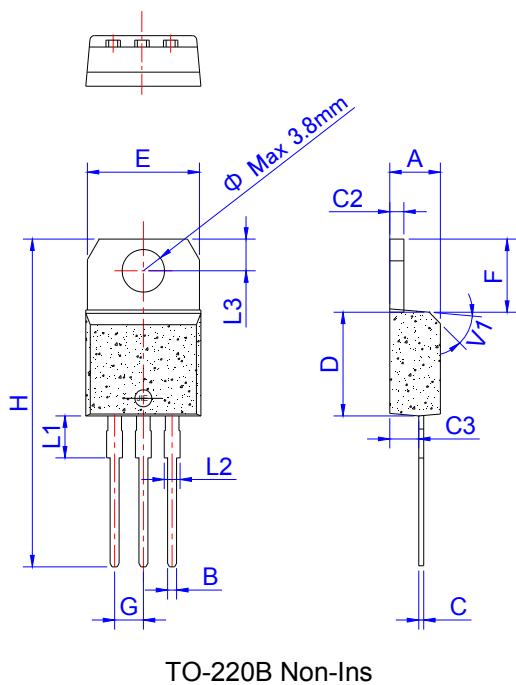
THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case(AC)	TO-251	°C/W
		TO-220B(Non-Ins)/ TO-220C	
		TO-220A(Ins)/ TO-220F(Ins)	
		TO-262	

ORDERING INFORMATION

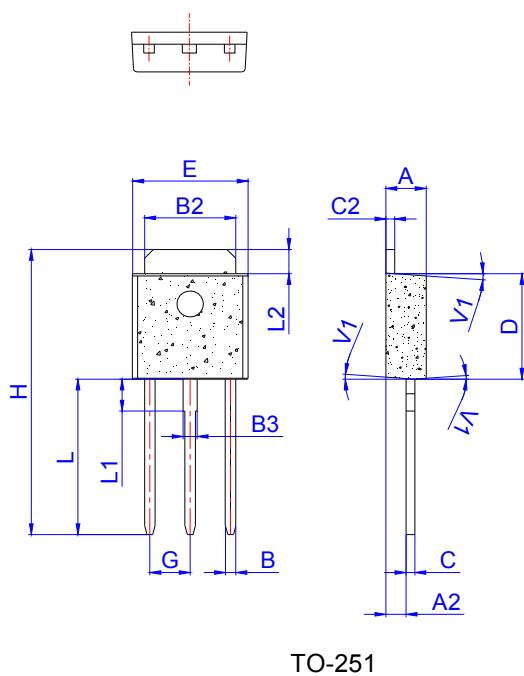
J	ST	137	D	-600	E
JieJie Microelectronics Co.,Ltd					
	Triacs				
		IT(RMS):8A			
C:TO-220C F:TO-220F(Ins) A:TO-220A(Ins) B:TO-220B(Non-Ins) H:TO-251 D:TO-262 DTR:TO-262(Tape&Reel)					D:IGT1-3≤5mA IGT4≤10mA E:IGT1-3≤10mA IGT4≤25mA F:IGT1-3≤25mA IGT4≤70mA G:IGT1-3≤50mA IGT4≤100mA
					600:V _{DRM} /V _{RRM} ≥600V 800:V _{DRM} /V _{RRM} ≥800V

PACKAGE MECHANICAL DATA

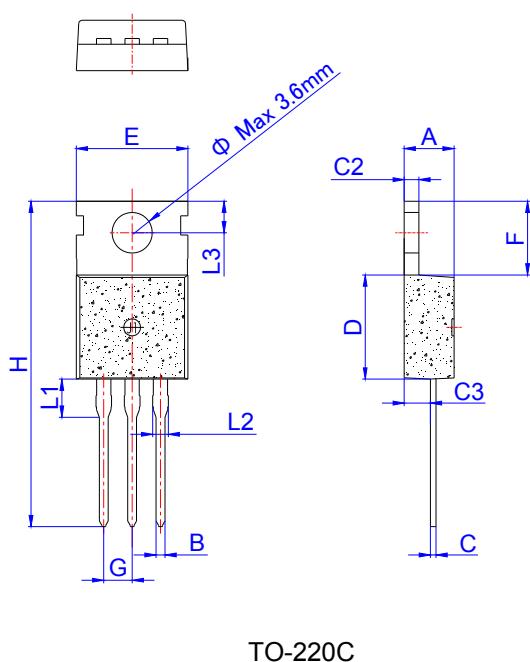


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.60		10.4	0.378		0.409
F	6.20		6.60	0.244		0.260
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

PACKAGE MECHANICAL DATA

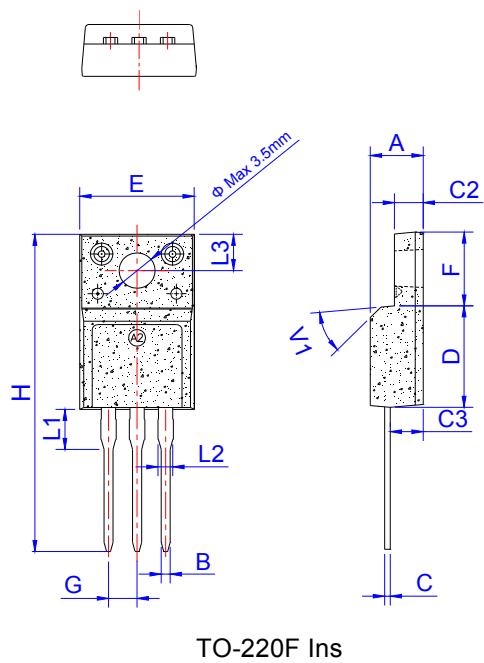


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.095
A2	0.90		1.20	0.035		0.047
B	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
B3	0.76		0.85	0.030		0.033
C	0.45		0.62	0.018		0.024
C2	0.48		0.62	0.019		0.024
D	6.00		6.20	0.236		0.244
E	6.40		6.70	0.252		0.264
G		2.30			0.091	
H	16.0		17.0	0.630		0.669
L	8.90		9.40	0.350		0.370
L1	1.80		1.90	0.071		0.075
L2	1.37		1.50	0.054		0.059
V1		4°			4°	

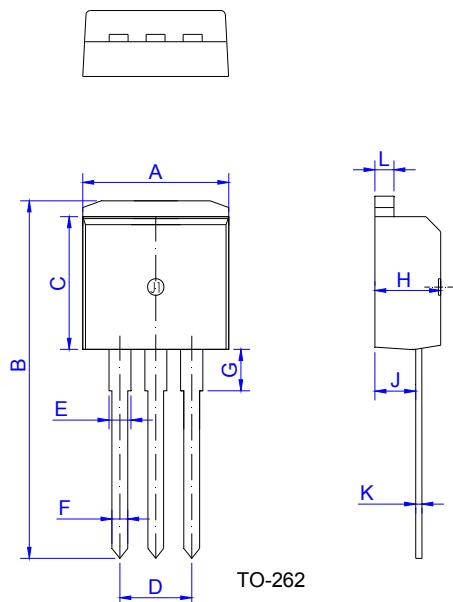


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.70		0.90	0.028		0.035
C	0.45		0.60	0.018		0.024
C2	1.23		1.32	0.048		0.052
C3	2.20		2.60	0.087		0.102
D	8.90		9.90	0.350		0.390
E	9.90		10.3	0.390		0.406
F	6.30		6.90	0.248		0.272
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.39			0.133	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
Φ		3.6			0.142	

PACKAGE MECHANICAL DATA

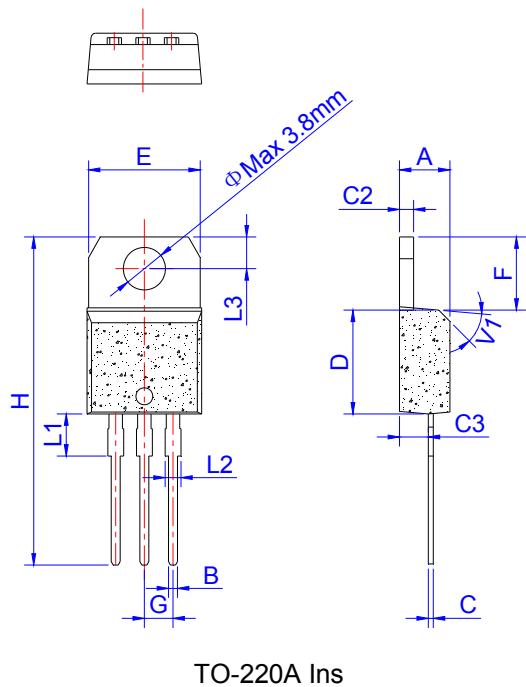


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.50		4.90	0.177		0.193
B	0.74	0.80	0.83	0.029	0.031	0.033
C	0.47		0.65	0.019		0.026
C2	2.45		2.75	0.096		0.108
C3	2.60		3.00	0.102		0.118
D	8.80		9.30	0.346		0.366
E	9.80		10.4	0.386		0.410
F	6.40		6.80	0.252		0.268
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.63			0.143	
L2	1.14		1.70	0.045		0.067
L3		3.30			0.130	
V1		45°			45°	



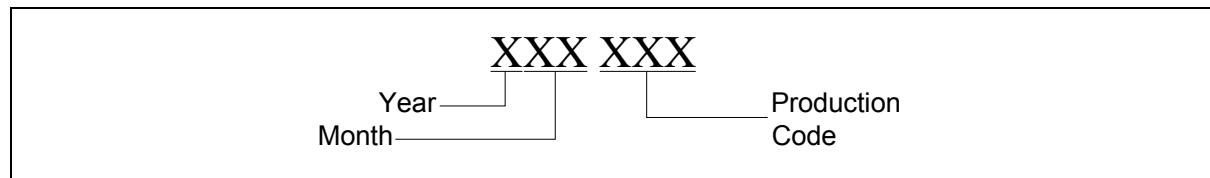
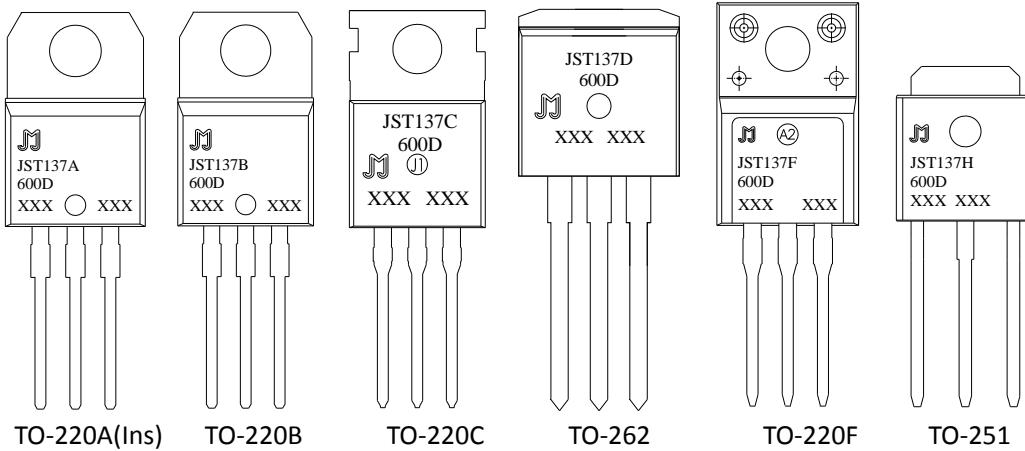
Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.95		10.20	0.392		0.402
B	23.25		23.45	0.915		0.923
C	8.90		9.10	0.35		0.358
D	5.00		5.20	0.197		0.205
E	1.20		1.35	0.047		0.053
F	0.80		0.85	0.031		0.033
G	3.30		3.60	0.130		0.142
H	4.45		4.55	0.175		0.179
J	2.50		2.70	0.098		0.106
K	0.38		0.42	0.015		0.017
L	1.25		1.29	0.049		0.051

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.80		10.4	0.386		0.409
F	6.55		6.95	0.258		0.274
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

MARKING



PACKAGE INFORMATION

PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-220A	TUBE	50	1,000	8,000
TO-220B	TUBE	50	1,000	8,000
TO-220C	TUBE	50	1,000	8,000
TO-220F	TUBE	50	1,000	8,000
TO-262	TUBE	50	1,000	6,000
TO-251	TUBE	50	4,000	32,000
PACKAGE	OUTLINE	REEL (PCS)	PER CARTON (PCS)	TAPE & REEL
TO-262	TAPING	800	4,000	13 inch

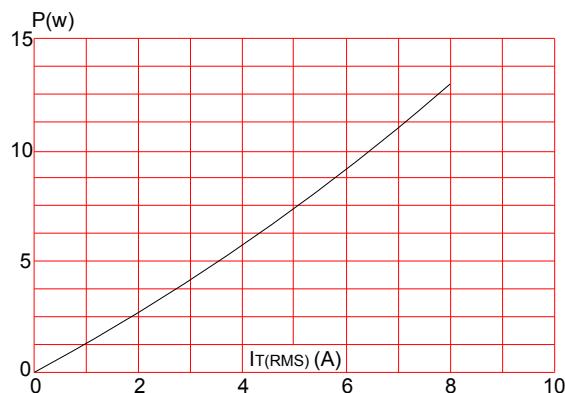
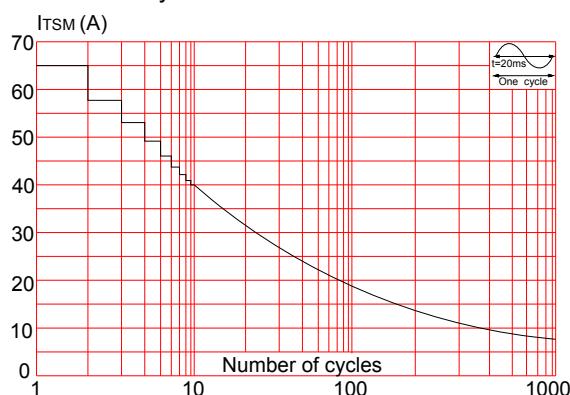
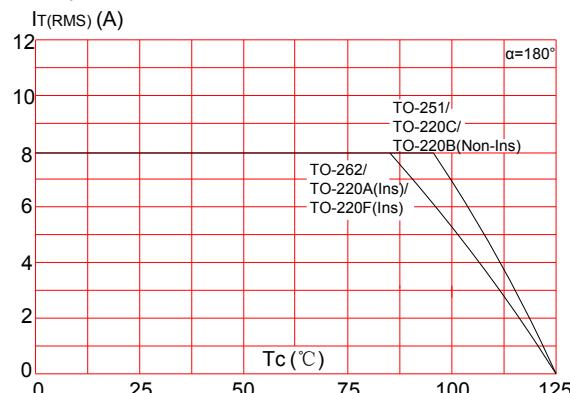
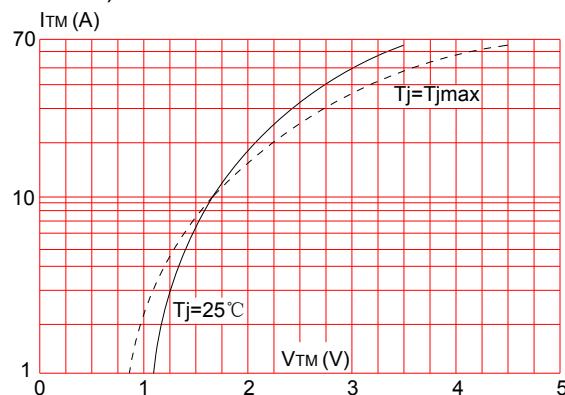
FIG.1: Maximum power dissipation versus RMS on-state current**FIG.3:** Surge peak on-state current versus number of cycles**FIG.2:** RMS on-state current versus case temperature**FIG.4:** On-state characteristics (maximum values)

FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t (I - II - III: $dI/dt < 50\text{A}/\mu\text{s}$; IV: $dI/dt < 10\text{A}/\mu\text{s}$)

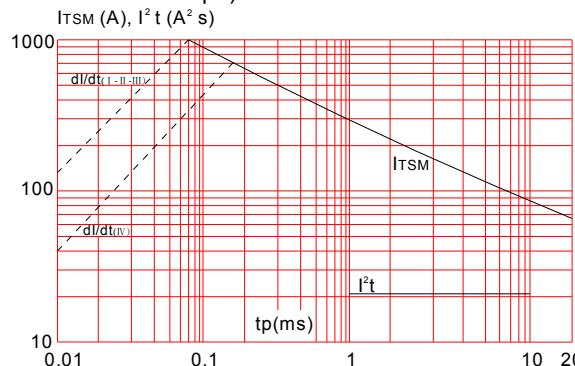


FIG.7: Relative variations of holding current versus junction temperature

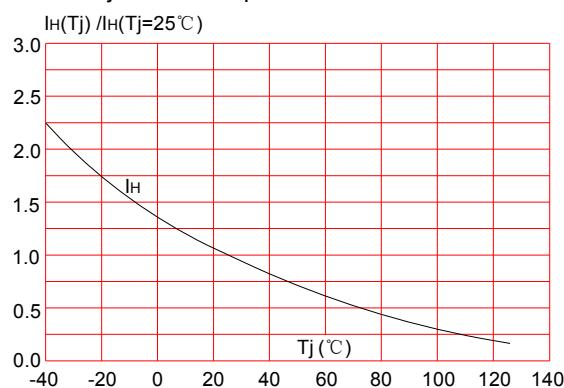


FIG.6: Relative variations of gate trigger current versus junction temperature

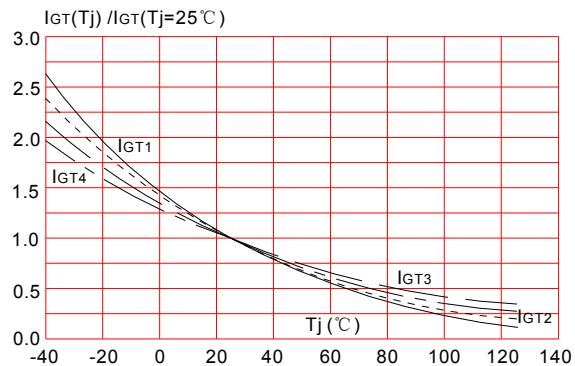
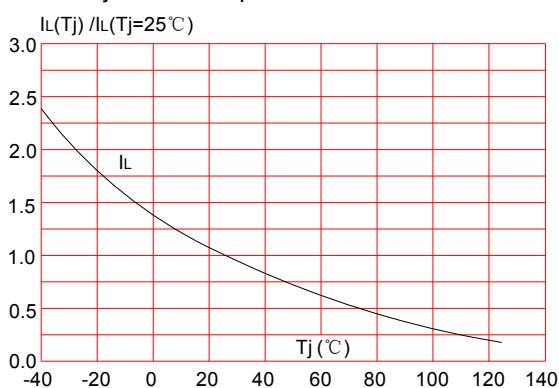


FIG.8: Relative variations of latching current versus junction temperature



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document is the thirteenth version which is made in 8-Apr.-2019. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright ©2019 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.