



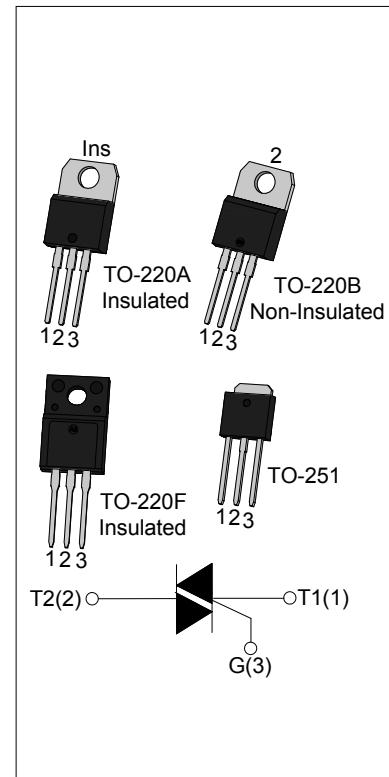
## JST06 Series 6A TRIACs

Rev.4.0

## DESCRIPTION:

With high ability to withstand the shock loading of large current, JST06 series triacs provide high dv/dt rate with strong resistance to electromagnetic interface. With high commutation performances, the products especially recommended for use on inductive load.

From all three terminals to external heatsink, JST06A provides a rated insulation voltage of 2500 V<sub>RMS</sub>, and JST06F provides a rated insulation voltage of 2000 V<sub>RMS</sub>, complying with UL standards (File ref: E252906). All the packages listed are RoHS compliant. (2011/65/EU)



## MAIN FEATURES

Symbol	Value	Unit
I <sub>T(RMS)</sub>	6	A
V <sub>DRM</sub> / V <sub>RRM</sub>	600/800	V

## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T <sub>stg</sub>	-40 - 150	°C
Operating junction temperature range	T <sub>j</sub>	-40 - 125	°C
Repetitive peak off-state voltage (T <sub>j</sub> =25°C)	V <sub>DRM</sub>	600/800	V
Repetitive peak reverse voltage (T <sub>j</sub> =25°C)	V <sub>RRM</sub>	600/800	V
RMS on-state current	I <sub>T(RMS)</sub>	6	A
TO-220A(Ins)/ TO-220F(Ins)/ TO-251 (T <sub>c</sub> =100°C) TO-220B(Non-Ins) (T <sub>c</sub> =105 °C)			
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I <sub>TSM</sub>	60	A
I <sup>2</sup> t value for fusing (tp=10ms)	I <sup>2</sup> t	18	A <sup>2</sup> s
Critical rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )	I - II - III	50	A/μs
	IV	10	

Peak gate current	I <sub>GM</sub>	2	A
Average gate power dissipation	P <sub>G(AV)</sub>	1	W
Peak gate power	P <sub>GM</sub>	5	W

**ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25°C unless otherwise specified)**

Symbol	Test Condition	Quadrant		Value		Unit
				C	B	
I <sub>GT</sub>	V <sub>D</sub> =12V R <sub>L</sub> =30Ω	I - II - III	MAX	25	50	mA
		IV		50	70	
V <sub>GT</sub>	ALL		MAX	1.5		V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125°C R <sub>L</sub> =3.3KΩ	ALL	MIN	0.2		V
I <sub>L</sub>	I <sub>G</sub> =1.2I <sub>GT</sub>	I - III - IV	MAX	50	70	mA
		II		60	80	
I <sub>H</sub>	I <sub>TM</sub> =0.2A		MAX	40	60	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125°C		MIN	200	500	V/μs

**STATIC CHARACTERISTICS**

Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>TM</sub> =8.5A t <sub>p</sub> =380μs	T <sub>j</sub> =25°C	1.5	V
I <sub>DRM</sub>		T <sub>j</sub> =25°C	5	μA
I <sub>RRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =125°C	1	mA

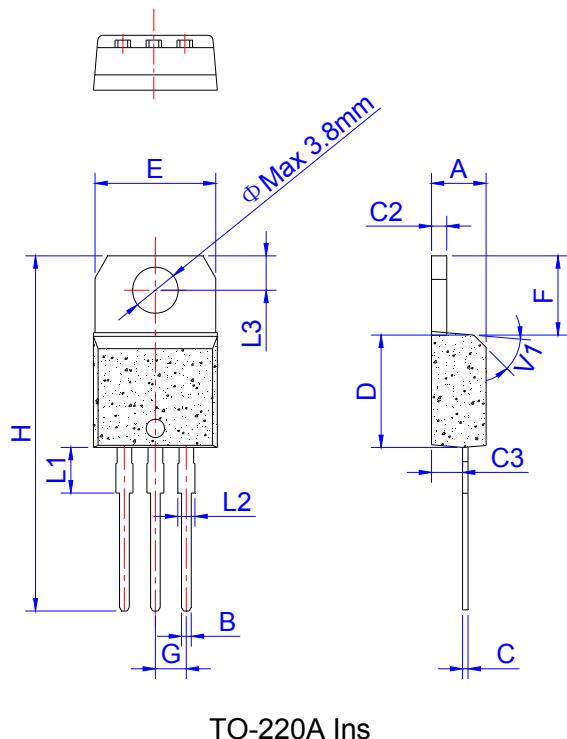
**THERMAL RESISTANCES**

Symbol	Parameter		Value	Unit
R <sub>th(j-c)</sub>	junction to case(AC)	TO-220A(Ins) TO-220F(Ins)	2.9	°C/W
		TO-220B(Non-Ins)	2.3	
		TO-251	2.7	

## ORDERING INFORMATION

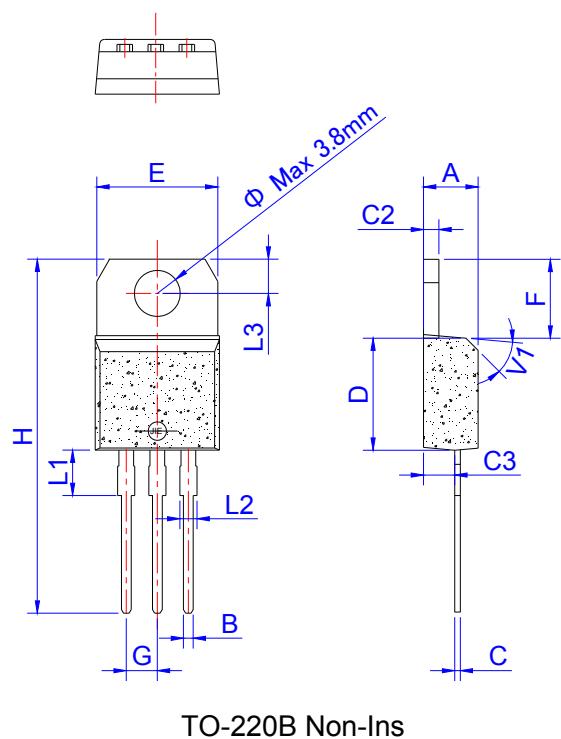
J	ST	06	A	-600	C
JieJie Microelectronics Co.,Ltd					
	Triacs				
		IT(RMS):6A			
			H:TO-251		
			A:TO-220A(Ins)		
			F:TO-220F(Ins)		
			B:TO-220B(Non-Ins)		
					C: IGT1-3≤25mA IGT4≤50mA B: IGT1-3≤50mA IGT4≤70mA
					600: V <sub>DRM</sub> / V <sub>RRM</sub> ≥600V 800: V <sub>DRM</sub> / V <sub>RRM</sub> ≥800V

## PACKAGE MECHANICAL DATA

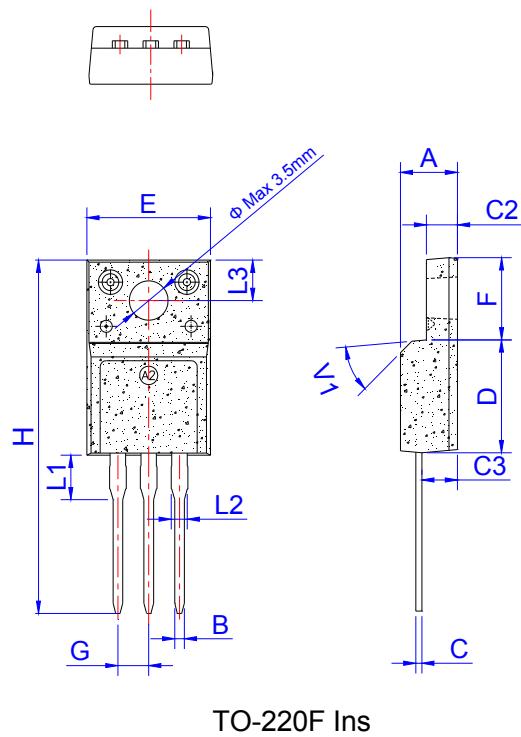


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.80		10.4	0.386		0.409
F	6.55		6.95	0.258		0.274
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

## PACKAGE MECHANICAL DATA

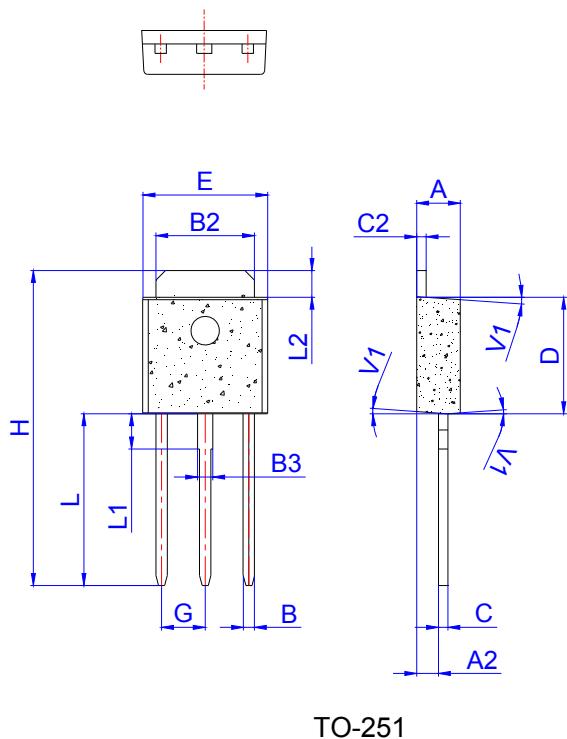


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.61		0.88	0.024		0.035
C	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.60		10.4	0.378		0.409
F	6.20		6.60	0.244		0.260
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.50		4.90	0.177		0.193
B	0.74	0.80	0.83	0.029	0.031	0.033
C	0.47		0.65	0.019		0.026
C2	2.45		2.75	0.096		0.108
C3	2.60		3.00	0.102		0.118
D	8.80		9.30	0.346		0.366
E	9.80		10.4	0.386		0.410
F	6.40		6.80	0.252		0.268
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.63			0.143	
L2	1.14		1.70	0.045		0.067
L3		3.30			0.130	
V1		45°			45°	

## PACKAGE MECHANICAL DATA

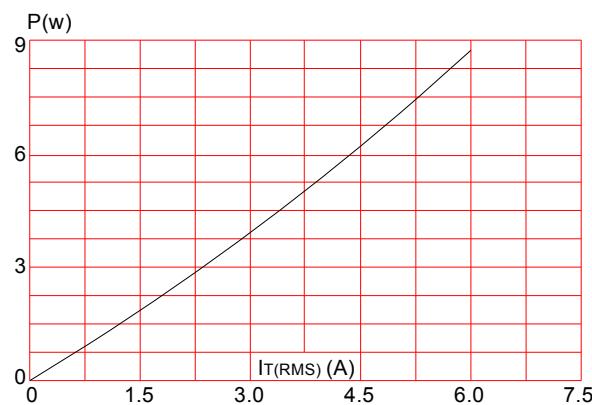


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.095
A2	0.90		1.20	0.035		0.047
B	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
B3	0.76		0.85	0.030		0.033
C	0.45		0.62	0.018		0.024
C2	0.48		0.62	0.019		0.024
D	6.00		6.20	0.236		0.244
E	6.40		6.70	0.252		0.264
G		2.30			0.091	
H	16.0		17.0	0.630		0.669
L	8.90		9.40	0.350		0.370
L1	1.80		1.90	0.071		0.075
L2	1.37		1.50	0.054		0.059
V1		4°			4°	

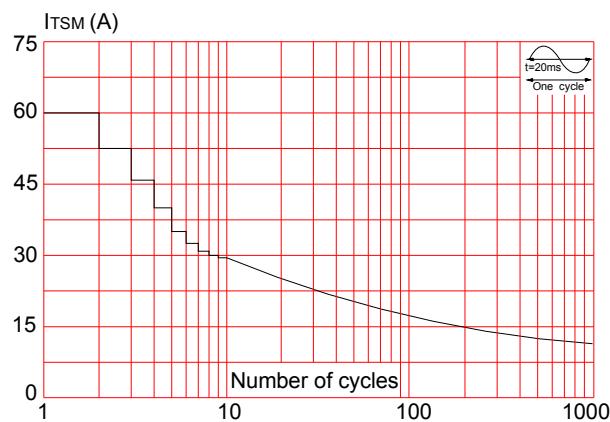
## PACKAGE INFORMATION

PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-220A	TUBE	50	1,000	8,000
TO-220B	TUBE	50	1,000	8,000
TO-220F	TUBE	50	1,000	8,000
TO-251	TUBE	80	4,000	32,000

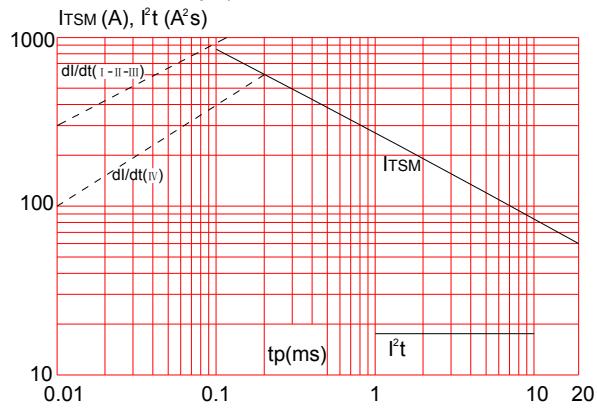
**FIG.1:** Maximum power dissipation versus RMS on-state current



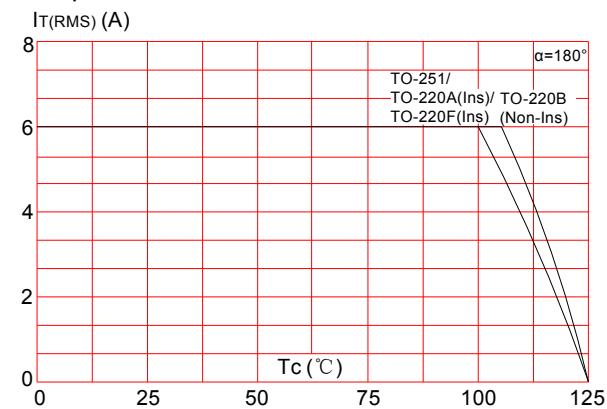
**FIG.3:** Surge peak on-state current versus number of cycles



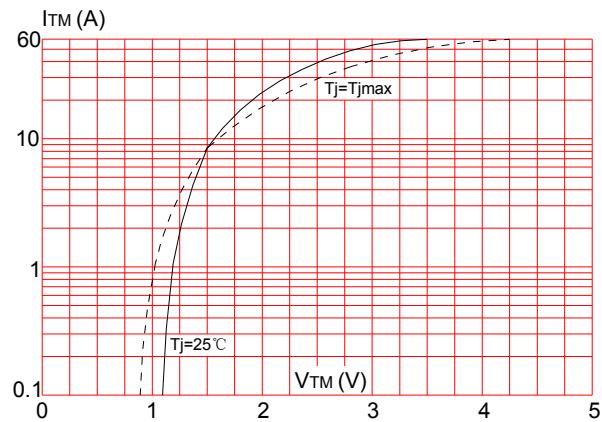
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 20\text{ms}$ , and corresponding value of  $I^2t$  ( $I - II - III: dI/dt < 50\text{A}/\mu\text{s}; IV: dI/dt < 10\text{A}/\mu\text{s}$ )



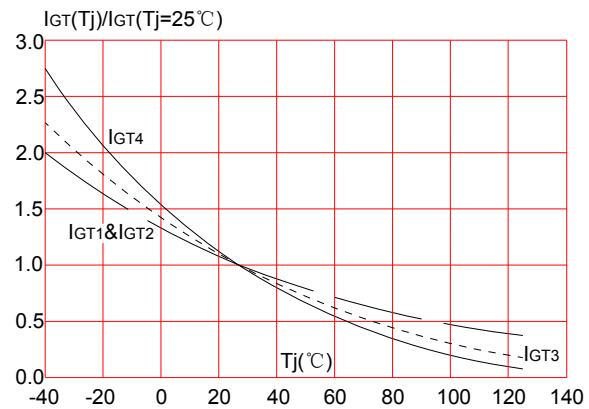
**FIG.2:** RMS on-state current versus case temperature

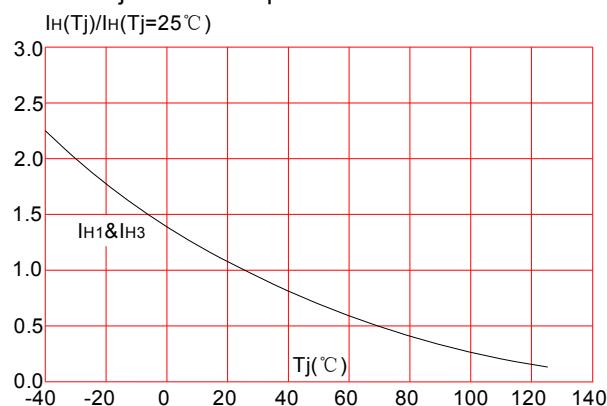
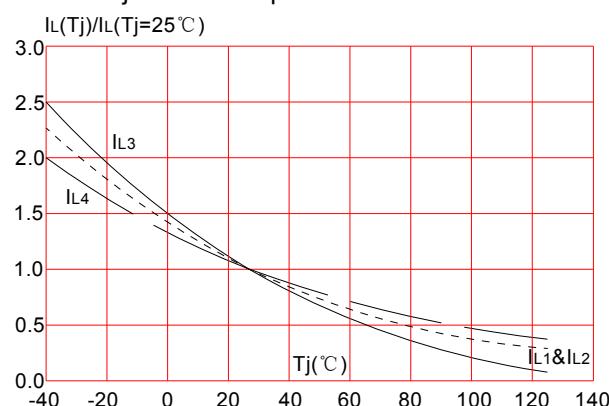


**FIG.4:** On-state characteristics (maximum values)



**FIG.6:** Relative variations of gate trigger current versus junction temperature



**FIG.7:** Relative variations of holding current versus junction temperature**FIG.8:** Relative variations of latching current versus junction temperature

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the fourth version which is made in 30-Sep.-2019. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright ©2019 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.