



## Description

### JMT N-channel Enhancement Mode Power MOSFET

#### Features

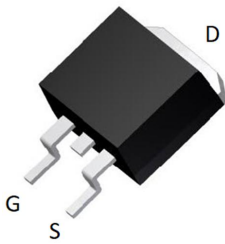
- 40V, 190A  
 $R_{DS(ON)} < 2.6m\Omega @ V_{GS} = 10V$
- Advanced Trench Technology
- Provide Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

#### Application

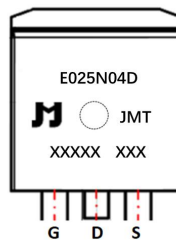
- Load Switch
- PWM Application
- Power management



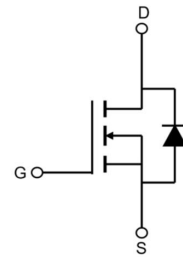
*100% UIS TESTED!*  
*100% ΔVds TESTED!*



TO-263 top view



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
JMTE025N04D	JMTE025N04D	TAPING	TO-263	13inch	1000	5000

## Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.	Units
$V_{DSS}$	Drain-Source Voltage	40	V
$V_{GSS}$	Gate-Source Voltage	$\pm 25$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ C$	190
		$T_C = 100^\circ C$	124
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>	760	A
EAS	Single Pulsed Avalanche Energy <sup>note2</sup>	576	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ C$	197
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.76	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +175	$^\circ C$



## Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V,	-	-	1.0	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±25V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2	3	4	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note3</small>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	-	1.9	2.6	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1.0MHz	-	9060	-	pF
C <sub>oss</sub>	Output Capacitance		-	1000	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	666	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =20V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V	-	145	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	30	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge		-	37	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =20V, I <sub>D</sub> =30A, R <sub>L</sub> =1Ω, R <sub>GEN</sub> =3Ω, V <sub>GS</sub> =10V	-	39	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	56	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	108	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	71	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	190	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	760	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =30A	-	-	1.2	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	T <sub>J</sub> =25°C, I <sub>F</sub> =20A, dI/dt=100A/μs	-	50	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	81	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

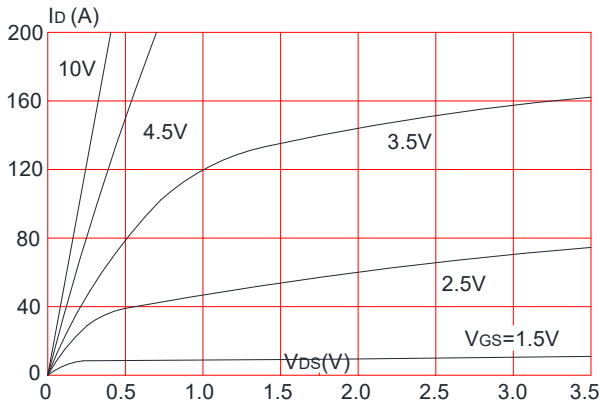
2. EAS condition : T<sub>J</sub>=25°C, V<sub>DD</sub>=50V, V<sub>G</sub>=10V, L=0.5mH, R<sub>g</sub>=25Ω, I<sub>AS</sub>=48A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

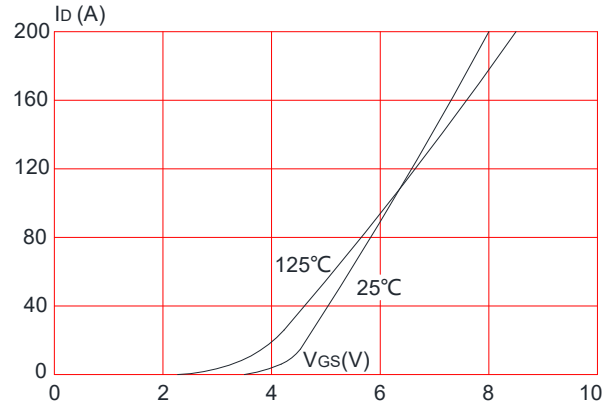


## Typical Performance Characteristics

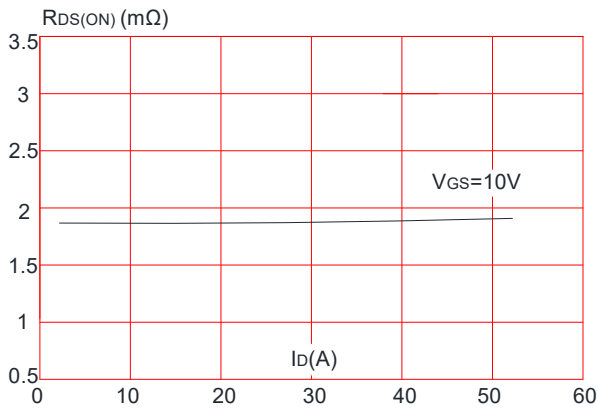
**Figure 1: Output Characteristics**



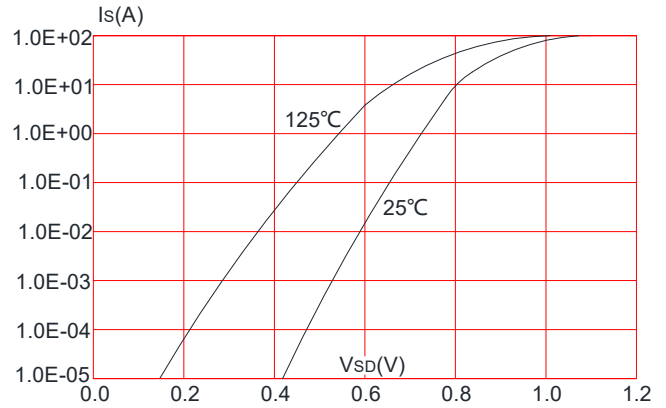
**Figure 2: Typical Transfer Characteristics**



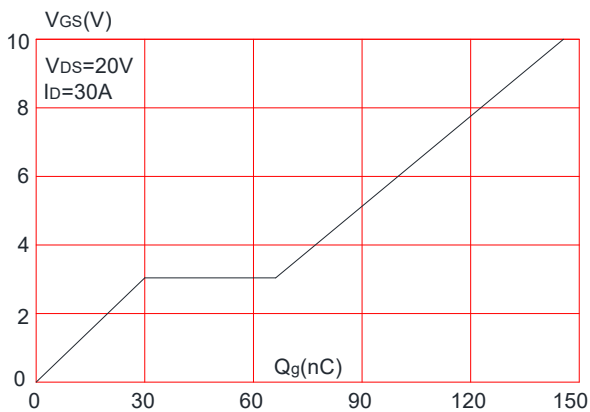
**Figure 3: On-resistance vs. Drain Current**



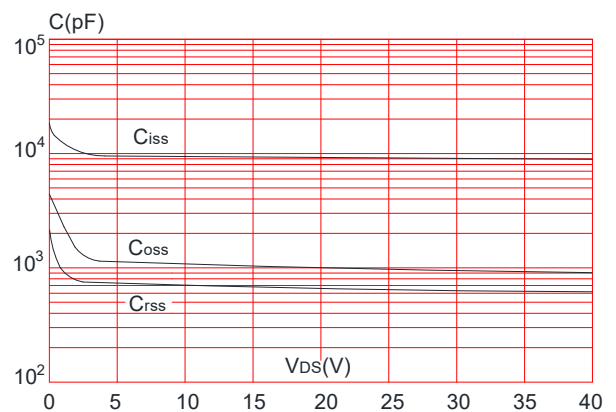
**Figure 4: Body Diode Characteristics**



**Figure 5: Gate Charge Characteristics**



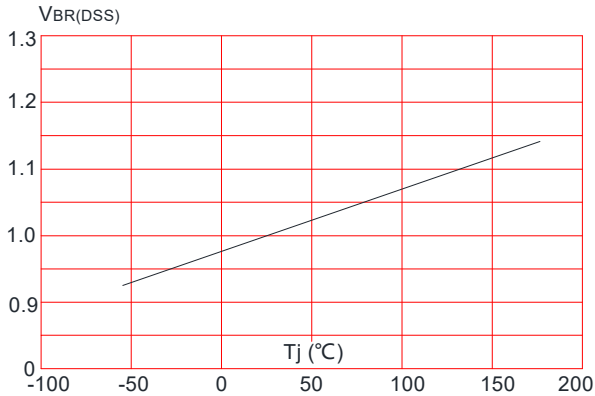
**Figure 6: Capacitance Characteristics**



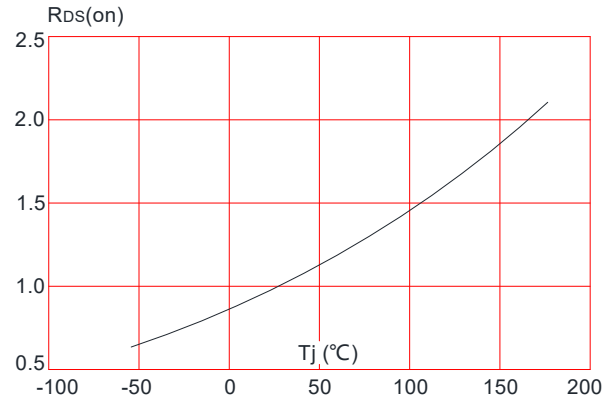


# JMTE025N04D

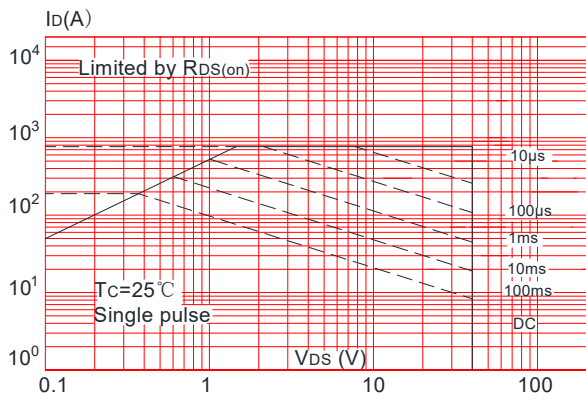
**Figure 7: Normalized Breakdown Voltage vs. Junction Temperature**



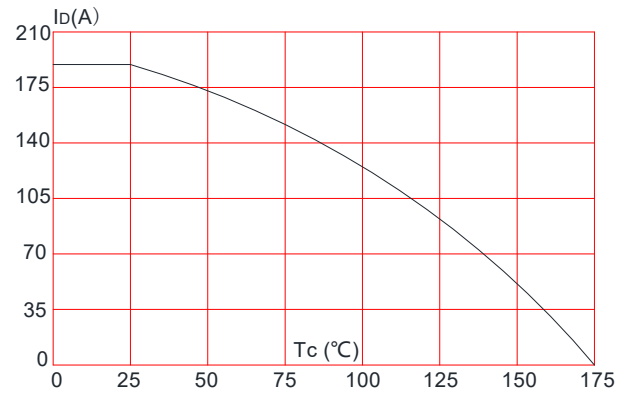
**Figure 8: Normalized on Resistance vs. Junction Temperature**



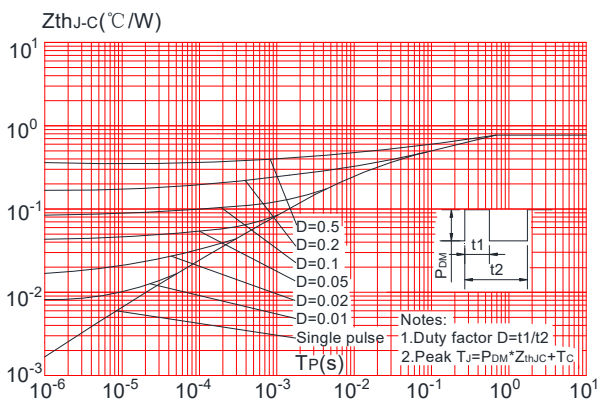
**Figure 9: Maximum Safe Operating Area**



**Figure 10: Maximum Continuous Drain Current vs. Case Temperature**



**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case**



## Test Circuit

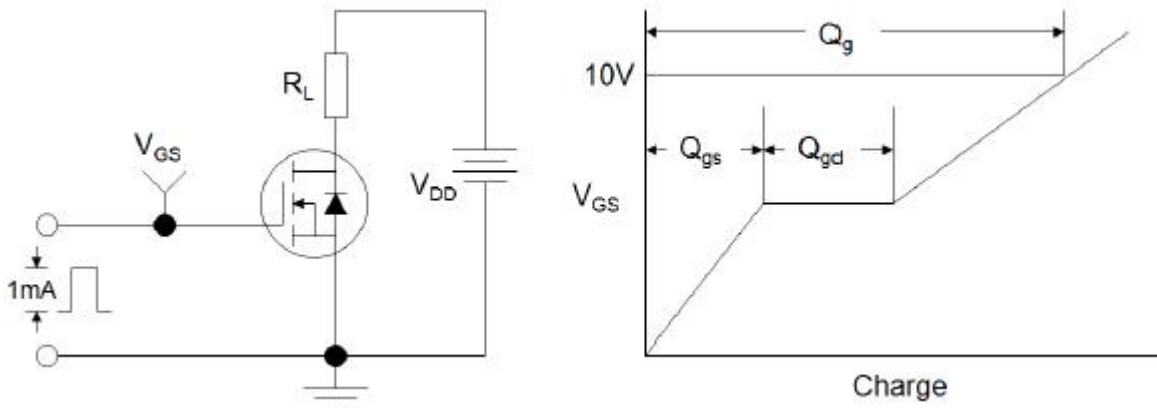


Figure1:Gate Charge Test Circuit & Waveform

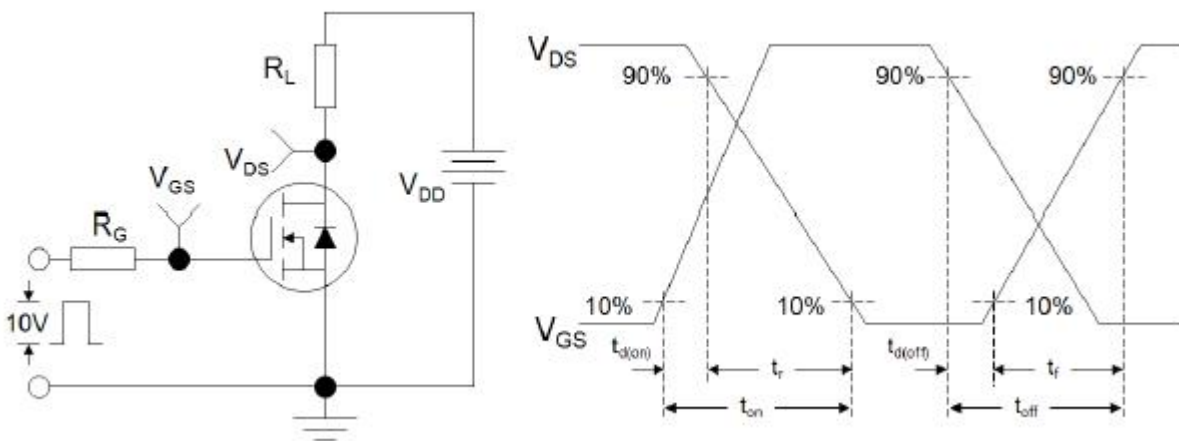


Figure 2: Resistive Switching Test Circuit & Waveforms

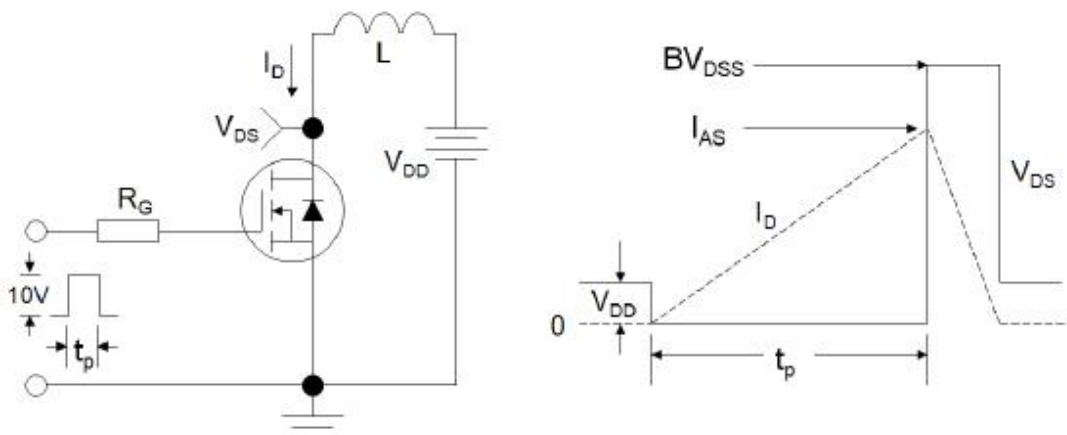
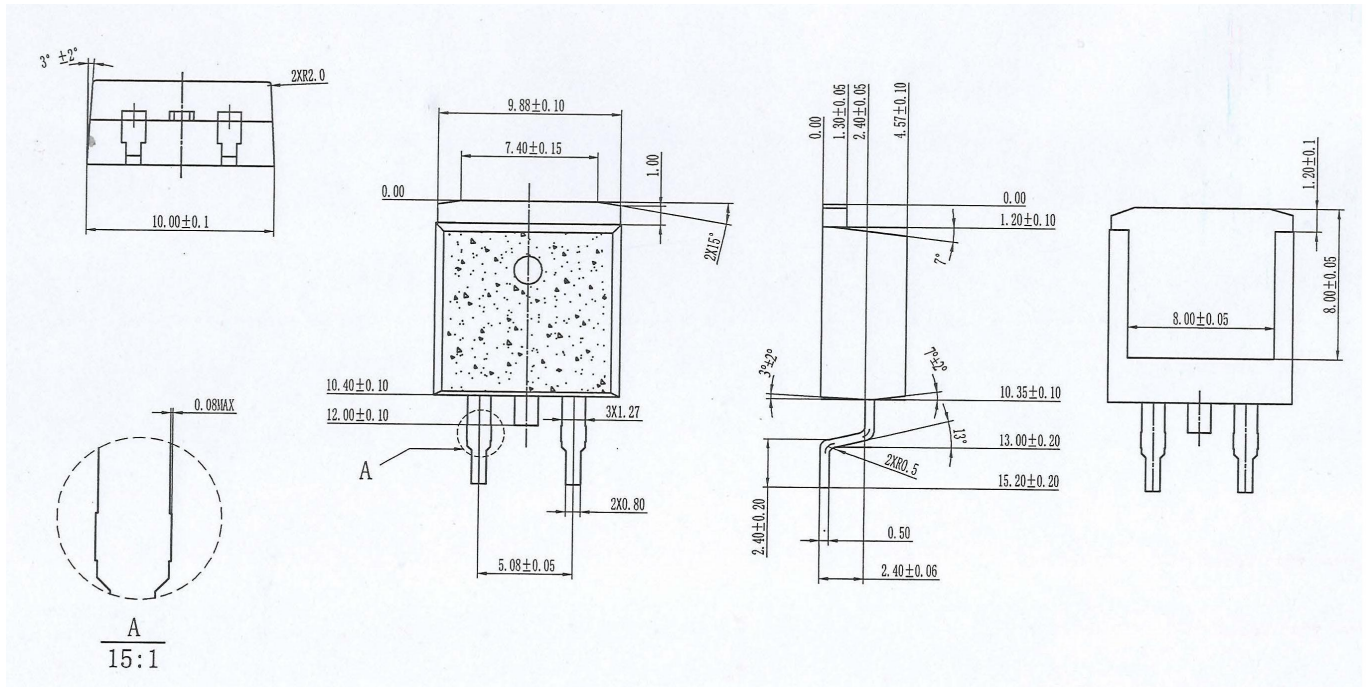


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



## Package Mechanical Data-TO-263



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