



30V 1.2mΩ N-Ch Power MOSFET

Features

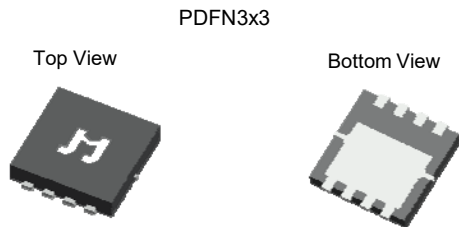
- Ultra-low $R_{DS(ON)}$
- High Current Capability
- ESD Enhanced to HBM Rating up to 1.5kV
- 100% UIS Tested, 100% Rg Tested

Applications

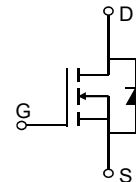
- Power Management in Computing, CE, IE 4.0, Communications
- Current Switching in DC/DC & AC/DC Sub-systems
- Motor Driving, Quick/Wireless Charging

Product Summary

Parameter	Typ.	Unit
V_{DS}	30	V
$V_{GS(th)}$	1.7	V
I_D (@ $V_{GS} = 10V$) ⁽¹⁾	145	A
$R_{DS(ON)}$ (@ $V_{GS} = 10V$)	1.2	mΩ
$R_{DS(ON)}$ (@ $V_{GS} = 4.5V$)	2.0	mΩ



Pin Configuration
Top View

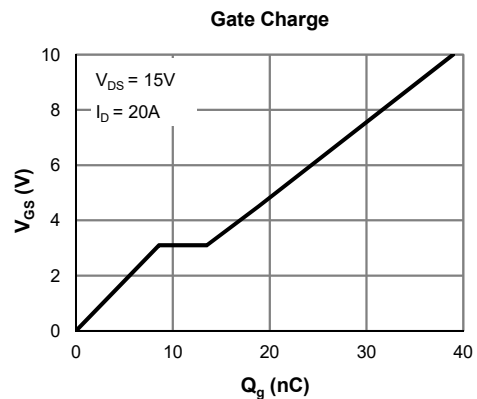
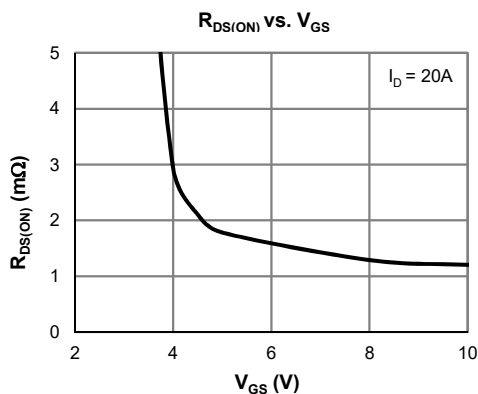


Ordering Information

Device	Package	# of Pins	Marking	MSL	T_J (°C)	Media	Quantity (pcs)
JMSL0302AU-13	PDFN3x3	8	SL0302A	1	-55 to 150	13-inch Reel	3000

Absolute Maximum Ratings (@ $T_A = 25^\circ C$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	30	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ⁽¹⁾	I_D	$T_C = 25^\circ C$	145
		$T_C = 100^\circ C$	91
Continuous Drain Current ⁽⁶⁾	I_D	80	A
Pulsed Drain Current ⁽²⁾	I_{DM}	450	A
Avalanche Current ⁽³⁾	I_{AS}	45	A
Avalanche Energy ⁽³⁾	E_{AS}	101	mJ
Power Dissipation ⁽⁴⁾	P_D	$T_C = 25^\circ C$	50
		$T_C = 100^\circ C$	20
Junction & Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C



**Electrical Characteristics** (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

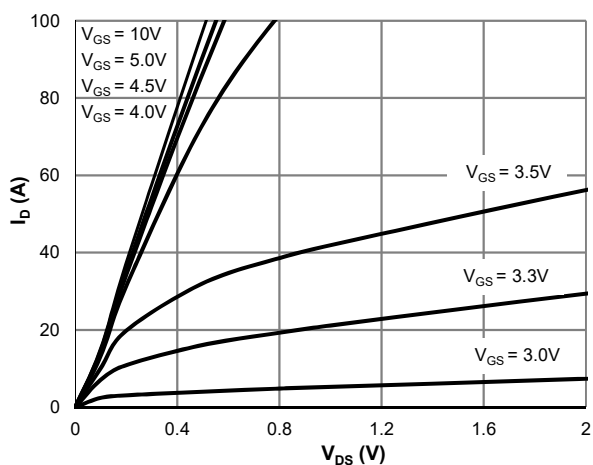
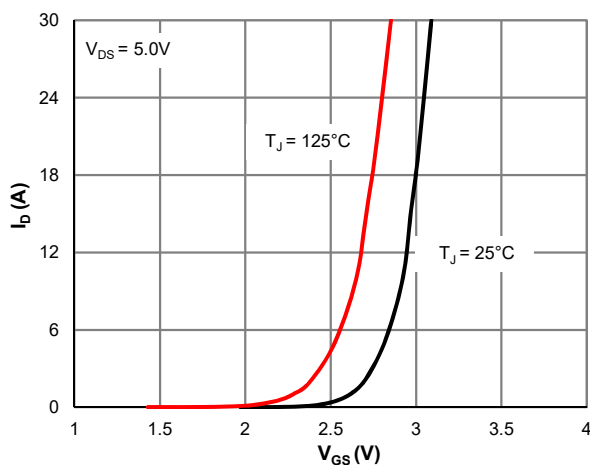
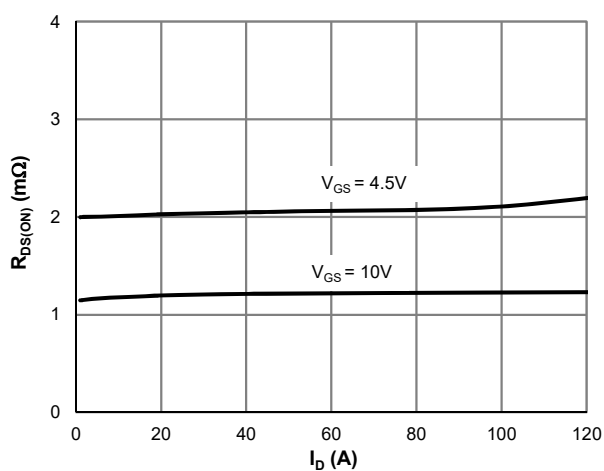
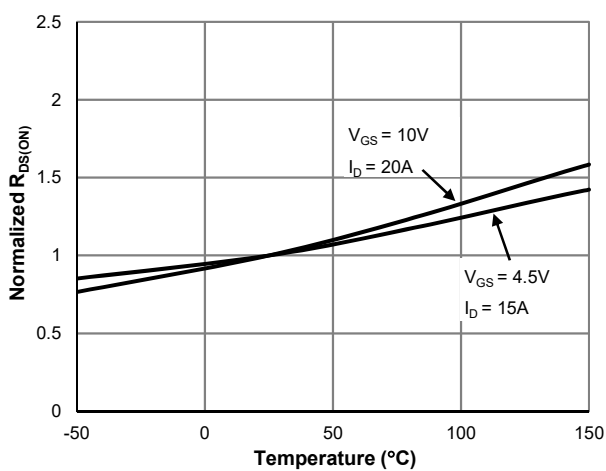
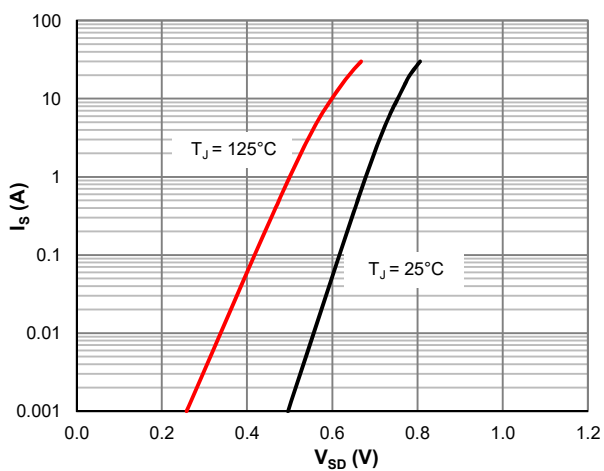
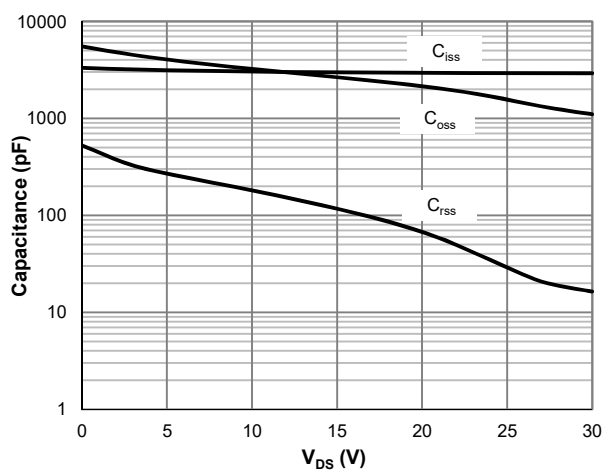
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 1\text{mA}, V_{GS} = 0\text{V}$	30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ\text{C}$			1.0 5.0	μA
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.2	1.7	2.5	V
Static Drain-Source ON-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$		1.2	1.5	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 15\text{A}$		2.0	2.9	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{DS} = 5\text{V}, I_D = 20\text{A}$		100		S
Diode Forward Voltage	V_{SD}	$I_S = 1\text{A}, V_{GS} = 0\text{V}$		0.68	1.0	V
Diode Continuous Current	I_S	$T_C = 25^\circ\text{C}$			50	A
DYNAMIC PARAMETERS ⁽⁵⁾						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, V_{DS} = 15\text{V}, f = 1\text{MHz}$		2975		pF
Output Capacitance	C_{oss}			2650		pF
Reverse Transfer Capacitance	C_{rss}			117		pF
Gate Resistance	R_g	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$		1.4		Ω
SWITCHING PARAMETERS ⁽⁵⁾						
Total Gate Charge (@ $V_{GS} = 10\text{V}$)	Q_g	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 15\text{V}, I_D = 20\text{A}$		39		nC
Total Gate Charge (@ $V_{GS} = 4.5\text{V}$)	Q_g			19.0		nC
Gate Source Charge	Q_{gs}			8.6		nC
Gate Drain Charge	Q_{gd}			5.0		nC
Turn-On Delay Time	$t_{D(on)}$	$V_{GS} = 10\text{V}, V_{DS} = 15\text{V}$ $R_L = 0.75\Omega, R_{GEN} = 3\Omega$		6.0		ns
Turn-On Rise Time	t_r			9.0		ns
Turn-Off Delay Time	$t_{D(off)}$			26		ns
Turn-Off Fall Time	t_f			10.0		ns
Body Diode Reverse Recovery Time	t_{rr}		$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		51	
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		57		nC

Thermal Performance

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	50	65	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.0	2.5	$^\circ\text{C}/\text{W}$

Notes:

1. Computed continuous current assumes the condition of T_{J_Max} while the actual continuous current depends on the thermal & electro-mechanical application board design.
2. This single-pulse measurement was taken under $T_{J_Max} = 150^\circ\text{C}$.
3. This single-pulse measurement was taken under the following condition [L = 100 μH , $V_{GS} = 10\text{V}$, $V_{DS} = 30\text{V}$] while its value is limited by $T_{J_Max} = 150^\circ\text{C}$.
4. The power dissipation P_D is based on $T_{J_Max} = 150^\circ\text{C}$.
5. This value is guaranteed by design hence it is not included in the production test.
6. Continuous current rating is limited by the package used.

Typical Electrical & Thermal Characteristics

Figure 1: Saturation Characteristics

Figure 2: Transfer Characteristics

Figure 3: $R_{DS(ON)}$ vs. Drain Current

Figure 4: $R_{DS(ON)}$ vs. Junction Temperature

Figure 5: Body-Diode Characteristics

Figure 6: Capacitance Characteristics

Typical Electrical & Thermal Characteristics

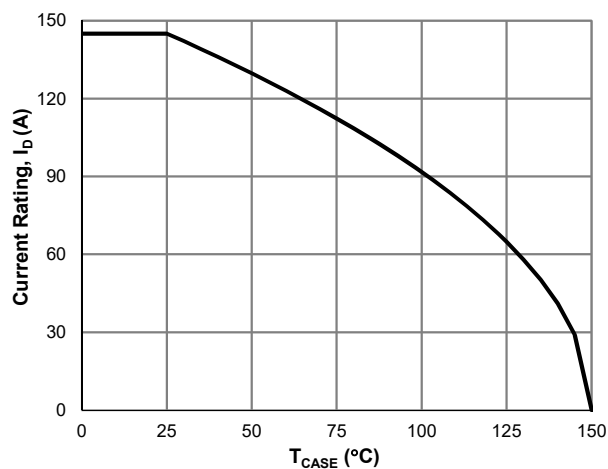


Figure 7: Current De-rating

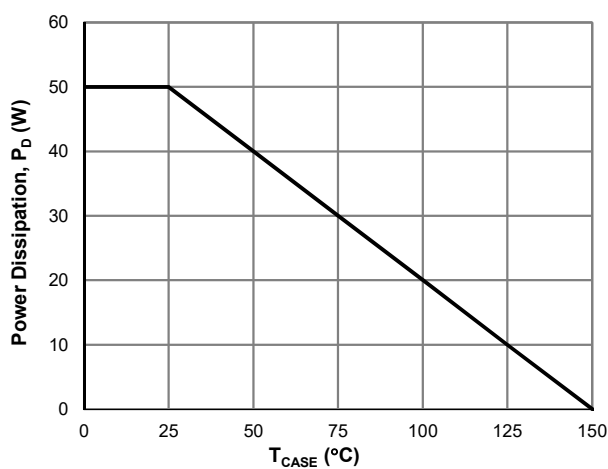


Figure 8: Power De-rating

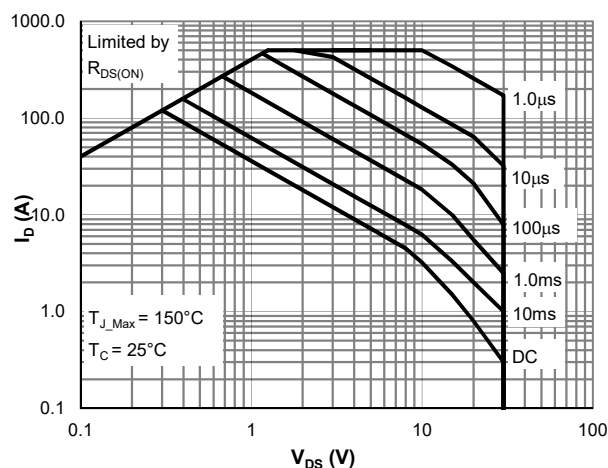


Figure 9: Maximum Safe Operating Area

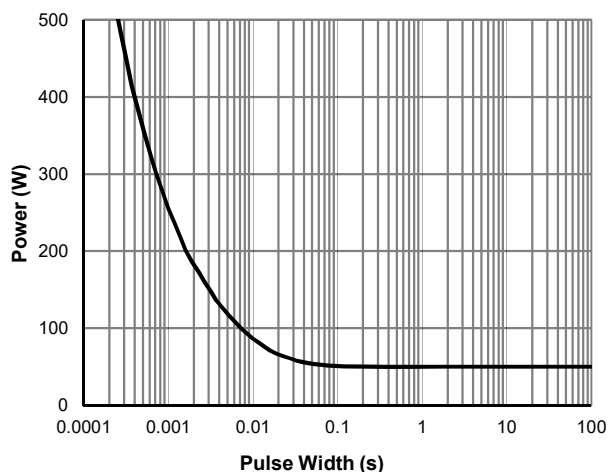


Figure 10: Single Pulse Power Rating, Junction-to-Case

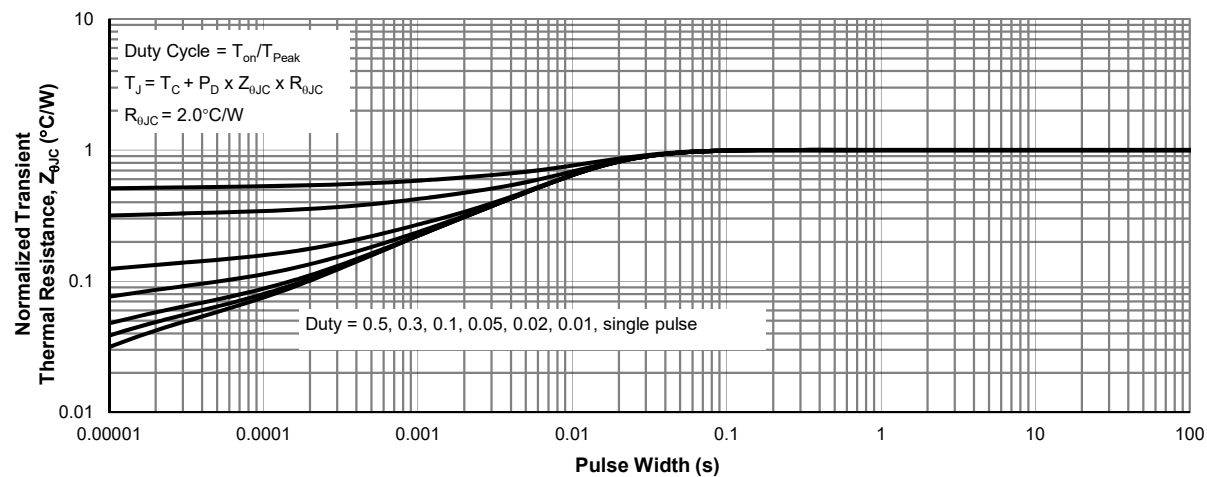
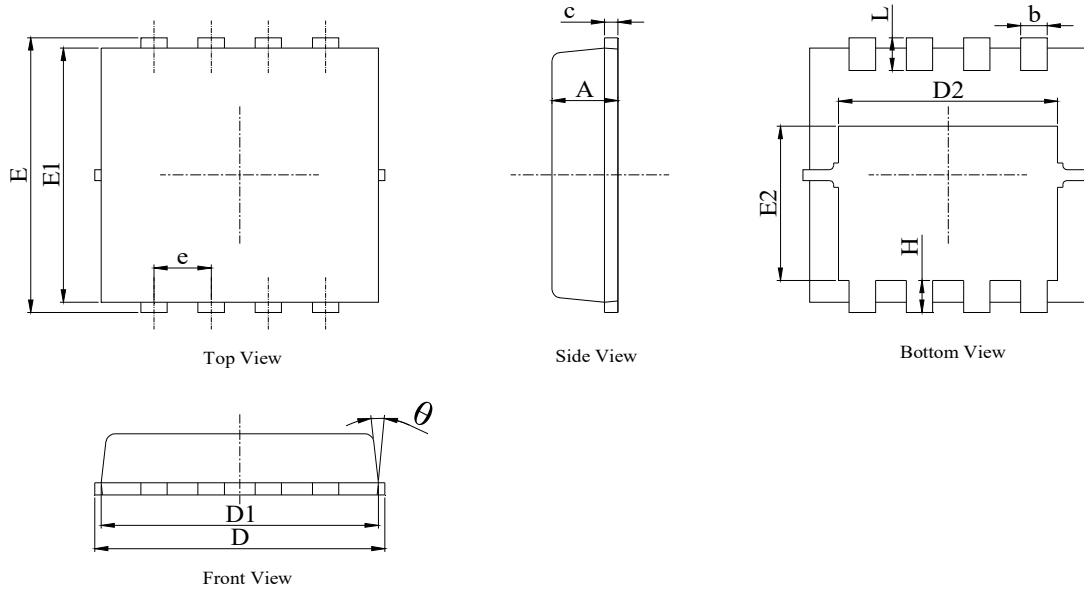
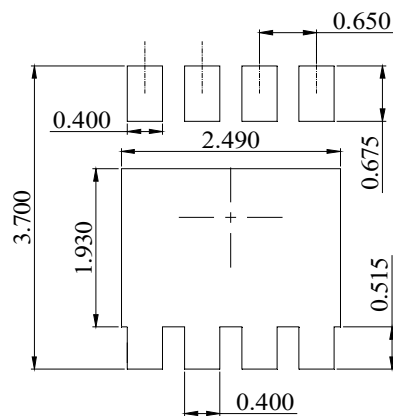


Figure 11: Normalized Maximum Transient Thermal Impedance

PDFN3x3 Package Information
Package Outline

NOTES:

1. Dimension and tolerance per ASME Y14.5M, 1994.
2. All dimensions in millimeter (angle in degree).
3. Dimensions $D1$ and $E1$ do not include mold flash protrusions or gate burrs.

DIM.	MILLIMETER	
	MIN.	MAX.
A	0.70	0.85
b	0.25	0.35
c	0.10	0.25
D	3.15	3.40
D1	3.00	3.25
D2	2.25	2.59
E	3.20	3.45
E1	3.00	3.22
E2	1.48	1.98
e	0.65 BSC	
H	0.30	0.58
L	0.25	0.50
θ	---	15°

Recommended Footprint


DIMENSIONS: MILLIMETERS